



STIC EIC 2100 Search Request Form

Today's Date:

What date would you like to use to limit the search?

Priority Date:

Other:

Name Chuck Kendall

AU 2122 Examiner # 75190

Room # 6C18 Phone 703 308 6608

Serial # 09483759

Format for Search Results (Circle One):

☒ PAPER

☐ DISK

☐ EMAIL

Where have you searched so far?

☒ USP

☒ DWPI

☒ EPO

☒ JPO

☐ ACM

☐ IBM TDB

☐ IEEE

☐ INSPEC

☐ SPI

☐ Other

Is this a "Fast & Focused" Search Request? (Circle One) ☒ YES ☐ NO

A "Fast & Focused" Search is completed in 2-3 hours (maximum). The search must be on a very specific topic and meet certain criteria. The criteria are posted in EIC2100 and on the EIC2100 NPL Web Page at <http://ptoweb/patents/stic/stic-ic2100.htm>.

What is the topic, novelty, motivation, utility, or other specific details defining the desired focus of this search? Please include the concepts, synonyms, keywords, acronyms, definitions, strategies, and anything else that helps to describe the topic. Please attach a copy of the abstract, background, brief summary, pertinent claims and any citations of relevant art you have found.

THE TOPIC IS IN PROGRAMMABLE INSTRUCTIONS FOR
DETECTING AND RESOLVING CIRCULAR FLOW PATHS
IN CHAIN ① LIMITATIONS SHOULD INCLUDE
i) HAVING A BUSY INDICATOR (STATUS INDICATOR OR MODE SHOWING)
PROCESSING OR EXECUTING
ii) Testing the BUSY INDICATOR OR (STATUS, MODE)
iii) BLOCKING/STOPPING OBJECT FROM RE-EXECUTING IF
INDICATOR SHOWS (BUSY OR PROCESSING/EXECUTING)
iv) IF NOT EXECUTING, BUSY INDICATOR OR INDICATOR
PERMITTING EXECUTING OR PROCESSING.

STIC Searcher Terese Esterfeld Phone 308-7795

Date picked up 1/30/04 1:45 PM Date Completed _____



Set	Items	Description
S1	13314837	DETECT? OR DETERMIN? OR RECOGNI? OR INTERROGAT? OR VERIF? - OR JUDGE? OR AUTHENTICAT? OR VALIDAT? OR IDENTIF?
S2	40139	FLOW()PATH OR DATA()FLOW
S3	2157221	BUSY()INDICATOR OR MODE OR STATUS
S4	21717102	TEST? OR DIAGNOS? OR EVALUAT? OR TRACK? OR ANALYS? OR ANAL- YZ?
S5	1529574	FUNCTIONING OR EXECUTING OR OPERATING OR PROCESSIONG
S6	12640184	OBJECT? OR EVENT? OR ELEMENT? OR PROGRAM? OR PACKET? OR BL- OCK?
S7	3579634	BLOCK? OR STOP? OR DELAY? OR HOLD? OR HALT? OR INTERRUPT? - OR RESTRICT?
S8	14492044	EXECUT? OR PROCESS? OR PERFORM?
S9	972	S1 (3N) S2
S10	116912	S3 (3N) S4
S11	3588	S10 (S) S5
S12	2	S9 (S) S10
S13	1	S9 (S) S11
S14	985	S11 (S) S6
S15	125	S14 (S) S7
S16	68	S15 (S) S8
S17	0	S9 (S) S16
S18	0	S9 (S) ("NOT" OR RE)()SI
S19	18	S9 (S) S3
S20	480	S9 (S) S4
S21	257	S20 (S) S6
S22	40	S21 (S) S7
S23	36	S22 (S) S8
S24	126	S12 OR S13 OR S16 OR S19 OR S22 OR S23
S25	105	S24 NOT PY>2000
S26	96	S25 NOT PD>20000114
S27	76	RD (unique items)
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27/3,K/1 (Item 1 from file: 2)

DIALOG(R)File 2:INSPEC

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6771780 INSPEC Abstract Number: C2001-01-6150G-015

Title: ATACOBOL-a COBOL test coverage analysis tool and its applications

Author(s): Sze, S.K.S.; Lyu, M.R.

Author Affiliation: Hong Kong & Shanghai Banking Corp., China

Conference Title: Proceedings 11th International Symposium on Software Reliability Engineering. ISSRE 2000 p.327-35

Publisher: IEEE Comput. Soc, Los Alamitos, CA, USA

Publication Date: 2000 Country of Publication: USA x+377 pp.

ISBN: 0 7695 0807 3 Material Identity Number: XX-2000-02532

U.S. Copyright Clearance Center Code: 0 7695 0807 3/2000/\$10.00

Conference Title: Proceedings of ISSRE 2000 International Symposium on Software Reliability Engineering

Conference Sponsor: Reliable Software Technol.; Telecordia Technol.; IBM Corp

Conference Date: 8-11 Oct. 2000 Conference Location: San Jose, CA, USA

Language: English

Subfile: C

Copyright 2000, IEE

Abstract: A coverage **testing** tool ATACOBOL (Automatic **Test Analysis** for COBOL) that applies a data flow coverage technique is developed for software development on an IBM System/390 mainframe. We show that the **data flow** coverage criteria can **identify** possible problematic paths that map to the actual **testing** semantic required by Y2K compliance software **testing**. However, the mainframe environment lacks **testing** tools that equip the data flow coverage measure. Up to the current implementation, ATACOBOL is able to **perform block** coverage, decision coverage and all-uses measures. We extend the rules of data flow coverage criteria to...

27/3,K/2 (Item 2 from file: 2)

DIALOG(R)File 2:INSPEC

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6661648 INSPEC Abstract Number: B2000-09-1265F-026, C2000-09-5130-019

Title: On random pattern testability of cryptographic VLSI cores

Author(s): Schubert, A.; Anheier, W.

Author Affiliation: Inst. fur Theor. Elektrotech. und Mikroelektronik, Bremen Univ., Germany

Journal: Journal of Electronic Testing: Theory and Applications

Conference Title: J. Electron. Test., Theory Appl. (Netherlands) vol.16, no.3 p.185-92

Publisher: Kluwer Academic Publishers,

Publication Date: June 2000 Country of Publication: Netherlands

CODEN: JTTER ISSN: 0923-8174

SICI: 0923-8174(200006)16:3L:185:RPTC;1-8

Material Identity Number: N821-2000-004

U.S. Copyright Clearance Center Code: 0923-8174/2000/\$18.00

Conference Title: European Test Workshop 1999

Conference Sponsor: IEEE Comput. Soc.; Univ. Stuttgart

Conference Date: 25-28 May 1999 Conference Location: Constance, Germany

Language: English

Subfile: B C

Copyright 2000, IEE

...Abstract: paper we show that the statistical properties of cryptographic algorithms are the reason for the excellent pseudorandom **testability** of cryptographic **processor** cores. The work is especially concerned with modern symmetric **block** encryption algorithms and their VLSI implementations. For the examination, typical basic operations of these cryptographic algorithms are categorized in classes and **analyzed** regarding their pseudorandom properties. Based on the results the pseudorandom properties of symmetric **block** ciphers can be **determined** by means of **data flow** graphs (DFG) and so-called predecessor operation

lists. This is demonstrated with a paradigm algorithm, the symmetric **block** cipher 3WAY. The results of the theoretical **analysis** lead to a so-called global BIST concept for cryptographic **processor** cores. This self- **test** approach is characterized by central pseudorandom pattern generators and signature registers at the primary inputs and outputs...

... BIST is exemplarily applied to an implementation of the 3WAY algorithm. Finally, the quality of the developed **test** approach is determined by fault simulations.

27/3,K/3 (Item 3 from file: 2)

DIALOG(R)File 2:INSPEC

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6650857 INSPEC Abstract Number: B2000-08-8150-006, C2000-08-7410B-087

Title: Expert system for automatic performance monitoring of utility power network

Author(s): Subramanian, R.; Mereikhi, A.S.; Marashi, A.; Samuel, A.; Al-Baya, A.H.; De Couto, C.A.; Puri, N.N.

Author Affiliation: Dept. of Water & Electr., Power & Desalination Plants, Abu Dhabi, United Arab Emirates

Conference Title: International Conference on Large High Voltage Electric Systems. CIGRE'98. Session Papers Part vol.10 p.12 pp. vol.10

Publisher: CIGRE, Paris, France

Publication Date: 1998 Country of Publication: France 17 vol.(106+218+140+150+138+134+194+106+56+138+70+66+106+136+146+216+98) pp.

Material Identity Number: XX-2000-00806

Conference Title: Proceedings of 37th Sessions (Large High Voltage Electric Systems)

Conference Date: 30 Aug.-5 Sept. 1998 Conference Location: Paris, France

Language: English

Subfile: B C

Copyright 2000, IEE

...Abstract: power system problems. While their potential uses are seen in a multitude of applications, fault analysis, alarm **processing**, substation control and monitoring, and power system protection are the four biggest segments of interest conceived for...

... markets. In fault analysis, an expert system is used to automatically deal with network disturbance and fault **events**, and helps protection and power system engineers gain immediate insights into details of the fault, network topology and system **operating** conditions. With the drastic reduction of manual work, this system produces a comprehensive and condensed report into...

...useful for operational staff to formulate a set of error-free guidelines for restoring service without undue **delay** of the disconnected plant and equipment following a wide-area blackout. This paper sets out in full to describe an expert system designed to **perform** a top down reasoning **analysis** using **status** information on relays, circuit breaker and teleprotection signalling equipment as well as fault waveform data files stored...

27/3,K/4 (Item 4 from file: 2)

DIALOG(R)File 2:INSPEC

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6554722 INSPEC Abstract Number: A2000-10-8734-030

Title: Distortion product otoacoustic emission test performance when both 2 f/sub 1/-f/sub 2/ and 2 f/sub 2/-f/sub 1/ are used to predict auditory status

Author(s): Gorga, M.P.; Nelson, K.; Davis, T.; Dorn, P.A.; Neely, S.T.

Author Affiliation: Boys Town Nat. Res. Hosp., Omaha, NE, USA

Journal: Journal of the Acoustical Society of America vol.107, no.4 p.2128-35

Publisher: Acoust. Soc. America through AIP,
Publication Date: April 2000 Country of Publication: USA
CODEN: JASMAN ISSN: 0001-4966
SICI: 0001-4966(200004)107:4L;2128:DPOE;1-B
Material Identity Number: J001-2000-004
U.S. Copyright Clearance Center Code: 0001-4966/2000/107(4)/2128/8/\$17.00
Language: English..
Subfile: A
Copyright 2000, IEE

Abstract: The **objective** of this study was to determine whether distortion product otoacoustic emission (DPOAE) test **performance**, defined as its ability to distinguish normal-hearing ears from those with hearing loss, can be improved...

... and for several primary frequency pairs can be used in a multivariate analysis to further optimize test **performance**. DPOAE and noise amplitudes were measured at 2 f/sub 1/-f/sub 2/ and 2 f...

... middle-ear function at the time of the DPOAE test, based on standard tympanometric measurements. Measurement-based **stopping** rules were used such that the test terminated when the noise floor around the 2 f/sub...

...of artifact-free averaging, whichever occurred first. Data were analyzed using clinical decision theory in which relative **operating** characteristics (ROC) curves were constructed and areas under the ROC curves were estimated. In addition, test **performance** was assessed by selecting the criterion value that resulted in a sensitivity of 90% and determining the...

... when f/sub 2/=audiometric frequency. In addition, multivariate analysis techniques were used to determine whether test **performance** can be optimized by using many variables to predict auditory status. As expected, DPOAEs were larger for...

...However, noise amplitudes were smaller for 2 f/sub 2/-f/sub 1/, but this effect was **restricted** to the lowest f/sub 2/ frequencies. A comparison of signal-to-noise ratios (SNR) within normal...

... for a fixed sensitivity increased only slightly when data from both DPs were used to predict auditory **status**. Multivariate **analyses**, in which the inputs included both DPs for several primary frequency pairs surrounding each audiometric frequency, produced the highest areas and specificities. Thus, DPOAE test **performance** was improved slightly by examining data at two DP frequencies simultaneously. This improvement was achieved at no...

...data obtained for several primary frequency pairs and then analyzed in a multivariate context, the best test **performance** was achieved. Excellent test **performance** (ROC) curve areas >0.95 and specificities >92% at all frequencies, including 500 Hz, were achieved for...

27/3,K/5 (Item 5 from file: 2)

DIALOG(R)File 2:INSPEC

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6511970 INSPEC Abstract Number: B2000-04-6120-003, C2000-04-7410F-034

Title: Computer simulation of symbol timing and carrier phase recovery of a 64-QAM demodulator

Author(s): Fang, T.T.; Jenn-Lainn Chu

Author Affiliation: Dept. of Electr. Eng., Nat. Chung Cheng Univ., Chiayi, Taiwan

Conference Title: Proceedings of the Eighteenth IASTED International Conference Modelling, Identification and Control p.213-15

Editor(s): Hamza, M.H.

Publisher: ACTA Press, Anaheim, CA, USA

Publication Date: 1999 Country of Publication: USA 611 pp.

ISBN: 0 88986 239 7 Material Identity Number: XX-1999-00794

Conference Title: Proceedings of 18th IASTED International Conference on Modelling Identification and Control

Conference Sponsor: IASTED

Conference Date: 15-18 Feb. 1999 Conference Location: Innsbruck, Austria

Language: English

Subfile: B C

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...Abstract: from the preamble mode into the data mode when a predetermined criterion is met. In the data **mode**, symbol **tracking** operation is applied to each of the consecutive **blocks** of 40 symbols while carrier phase tracking is **performed** seamlessly on all the received data, using decision feedback. With proper selection of **operating** parameters, the system can decode up to 100,000 data symbols under additive white Gaussian noise environment...

27/3,K/6 (Item 6 from file: 2)

DIALOG(R)File 2:INSPEC

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6389764 INSPEC Abstract Number: B1999-12-8360-036, C1999-12-5150-004

Title: Designing minimum cost VRM8.2/8.3 compliant converters

Author(s): Redl, R.; Erisman, B.

Author Affiliation: ELFI SA, Farvagny, Switzerland

Conference Title: Official Proceedings of the Thirteenth International HFPC '98 (High Frequency Power Conversion). Presented at Powersystems World '98. Conference and Exhibit p.172-81

Publisher: Intertec Int, Ventura, CA, USA

Publication Date: 1998 Country of Publication: USA 342 pp.

ISBN: 0 931033 70 5 Material Identity Number: XX-1998-03258

Conference Title: Proceedings of Power Systems World '98

Conference Date: 7-13 Nov. 1998 Conference Location: Santa Clara, CA, USA

Language: English

Subfile: B C

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Abstract: Providing the specified core voltage to a Pentium(R) II or other **processor** throughout all of its dynamic loading conditions is a challenge for which there are many advocates of solutions, but for which few have provided the **analysis**. Fast voltage-**mode** architectures have the appeal to the CPU power converter designer of not requiring a current-sensing resistor and **holding** the promise of the fastest possible recovery from a transient. But a worst case analysis of: (1) permissible load transients; (2) **operating** set-point voltage tolerance; and (3) transient response **performance**, tell another story. The cost, size, and **performance** optimized solution: (1) requires current sensing; (2) does not require a fast transient recovery; and yet (3...

... realized with the ADP3152/3 buck converter controller IC from Analog Devices. The cost and size reduction **objective** is realized in terms of the number of output capacitors required to keep the core voltage within...

27/3,K/7 (Item 7 from file: 2)

DIALOG(R)File 2:INSPEC

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6263036 INSPEC Abstract Number: C1999-07-6150G-010

Title: Dependency analysis of Ada programs

Author(s): Laski, J.; Stanley, W.; Hurst, J.

Author Affiliation: SofTools Inc., Rochester Hills, MI, USA

Journal: Ada Letters Conference Title: Ada Lett. (USA) vol.18, no.6 p.263-75

Publisher: ACM,

Publication Date: Nov.-Dec. 1998 Country of Publication: USA

CODEN: AALEE5 ISSN: 0736-721X

SICI: 0736-721X(199811/12)18:6L:263:DAP;1-R
Material Identity Number: K763-1999-002
U.S. Copyright Clearance Center Code: 0736-721X/98/0011...\$5.00
Conference Title: Proceedings of SIGAda '98 (formerly Tri-Ada) ACM SIGAda
Annual International Conference
Conference Sponsor: ACM SIGAda; SIGAPP; SIGBIO; SIGCAS; SIGCSE; SIGPLAN;
SIGSOFT, DC SIGAda; Baltimore SIGAda; Ada-Europe
Conference Date: 8-12 Nov. 1998 Conference Location: Washington, DC,
USA

Language: English

Subfile: C

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Abstract: The working hypothesis of the paper is the belief that software **testing** and **analysis** (STA) methods should be integrated around a common conceptual framework. An **analysis** of two potential candidates for such a framework, **program** dependencies and information flow relations, shows that the ideal framework should possess the mathematical elegance of the flow relations and the generality of **program** dependencies. However, **program** dependencies have originally been formulated for compiler optimization and their uncritical use in software engineering is lacking...

...is shown how the modified model can support descriptive and proscriptive (e.g. anomalies) queries about the **program** and, due to the clear separation of control flow from data flow and the lack of language **restrictions**, they are potentially applicable to a wider class of STA methods, including dynamic (**execution** based) **analysis**. Also, path **analysis**, a novel method for the identification of dependencies along individual **program** paths is proposed. It is shown that path **analysis** offers a more accurate model for procedure calls, allows one to **detect** otherwise undetectable **data flow** anomalies and can serve as a vehicle for the **analysis** of error creation and propagation in **testing** and debugging.

27/3,K/8 (Item 8 from file: 2)

DIALOG(R)File 2:INSPEC

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5621654 INSPEC Abstract Number: C9708-5220P-021

Title: A framework for detecting redundant data transmission on distributed memories by data flow analysis

Author(s): Xiaojie Li; Harada, K.

Author Affiliation: Dept. of Comput. Sci., Keio Univ., Yokohama, Japan

Conference Title: Parallel Computing: State-of-the-Art and Perspectives
p.295-302

Editor(s): D'Hollander, E.; Peters, F.J.; Joubert, G.R.; Trystram, D.

Publisher: Elsevier, Amsterdam, Netherlands

Publication Date: 1996 Country of Publication: Netherlands xvi+723
pp.

ISBN: 0 444 82490 1 Material Identity Number: XX95-02301

Conference Title: Proceedings of ParCo 95. Parallel Computing 95: State
of the Art Perspectives

Conference Sponsor: BARCO; BELGACOM; Cray; Elsevier Sci.; IBM

Conference Date: 19-22 Sept. 1995 Conference Location: Gent, Belgium

Language: English

Subfile: C

Copyright 1997, IEE

...Abstract: memory multiprocessors. In typical numerical applications, a significant portion of data communication are due to accesses to **blocks** of array **elements** or regions. Data communication overheads due to such traffic can be reduced by using compile-time information to detect data which can be reused. This paper presents a **data flow analysis** framework for **determining** the data sets which can be reused. First, the data availability information on a virtual **processor** grid is obtained. Then data sets which can be reused by a **processor** can be found. We use interval data flow **analysis** to obtain data availability information at each statement: this is useful for determining redundant data communication

on...

27/3,K/9 (Item 9 from file: 2)

DIALOG(R)File 2:INSPEC

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5467706 INSPEC Abstract Number: B9702-6150M-023, C9702-5640-024

Title: Test suite generation method considering of data flow anomalies in protocol testing

Author(s): Byeong Ho Oh; Sang Ho Lee

Journal: Journal of KISS(A) (Computer Systems and Theory) vol.23,
no.10 p.1041-9

Publisher: Korea Inf. Sci. Soc,

Publication Date: Oct. 1996 Country of Publication: South Korea

CODEN: CKNOF2 ISSN: 1226-2315

SICI: 1226-2315(199610)23:10L:1041:TSGM;1-Q

Material Identity Number: E345-96014

Language: Korean

Subfile: B C

Copyright 1997, IEE

Abstract: Data flow of protocol in conformance **testing** gives us much difficulty in generating more effective **test** suites. The difficulty is caused by range and number of variables. In this paper, data flow anomalies, that can be found by data flow **analysis process**, are integrated to control flow in order to generate valid circulating paths and **test** suites. All paths in terms of control flow are generated using Boolean algebra and invalid paths are **detected** through static **data flow analysis**. The integration **process** can be described as follows: Firstly, we **analyze** anomalous usage of variables in single transition **block**. Second, we compare the variables to all circulating paths. As a result, all invalid paths with respect...

... paths from all of the control flow paths are removed. Finally, we generate valid circular paths and **test** suites that considering data flow and control flow. In addition, we retrieve inappropriate UIOs in the view ...

27/3,K/10 (Item 10 from file: 2)

DIALOG(R)File 2:INSPEC

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5369251 INSPEC Abstract Number: C9610-7410B-172

Title: Design and implementation of multimedia environment for simulation

Author(s): Bustio, F.; Corcuera, P.; Mora, E.

Author Affiliation: Dept. of Appl. Math. & Comput. Sci., Cantabria Univ., Santander, Spain

Conference Title: Computer Aided Systems Theory - CAST '94. 4th International Workshop. Selected Papers p.268-77

Editor(s): Oren, T.I.; Klir, G.J.

Publisher: Springer-Verlag, Berlin, Germany

Publication Date: 1996 Country of Publication: West Germany ix+437 pp.

ISBN: 3 540 61478 8 Material Identity Number: XX96-02157

Conference Title: Proceedings of Computer Aided Systems Theory - CAST'94. 4th International Workshop

Conference Date: 16-20 May 1994 Conference Location: Ottawa, Ont., Canada

Language: English

Subfile: C

Copyright 1996, IEE

...Abstract: engineering. This paper presents a design methodology aimed at developing multimedia interfaces for the simulation of industrial **processes**, using an **object** oriented point of view. A division of its composing **elements** into three functional **blocks** is proposed. The design

methodology is based on the carrying out of a series of interrelated tasks, which from a software engineering perspective, gives maximum **performance**, both for the development **process** and its subsequent updating. The methodology was applied to the simulation of a nuclear power plant with...

... batch mode and interactive mode) are proposed, determined by the simulation modules used. In each case, the **operating mode** is described and **analyzed** from the **programmer's** and the user's point of view.

27/3,K/11 (Item 11 from file: 2)

DIALOG(R)File 2:INSPEC

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4904715 INSPEC Abstract Number: C9504-7310-039

Title: Practical performance of some parallel variable stepsize ODE solvers

Author(s): Ghasem-Aghaee, N.; Birta, L.G.; King, D.

Author Affiliation: Dept. of Comput. Eng., Isfahan Univ., Iran

p.3-8

Editor(s): Pace, D.K.; Fayek, A.-M.

Publisher: SCS, San Diego, CA, USA

Publication Date: 1994 Country of Publication: USA xix+972 pp.

Conference Title: Proceedings of 1994 Summer Computer Simulation Conference

Conference Sponsor: SCS

Conference Date: 18-20 July 1994 Conference Location: San Diego, CA, USA

Language: English

Subfile: C

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...Abstract: literature. These methods have considerable potential relevance in simulation studies of large-scale continuous systems. Very few **performance** evaluation studies of the methods on actual multiprocessor hardware have been reported. Our purpose in this paper is to present the results of such a **performance** study for a particular class of parallel ODE solvers **executing** on a particular MIMD hardware architecture. The class of methods we consider is of the **block** predictor-corrector family, implemented in a variable stepsize **mode**. A **performance evaluation** is presented in terms of solution speed on the multiprocessor hardware in question. The impact of various...

27/3,K/12 (Item 12 from file: 2)

DIALOG(R)File 2:INSPEC

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04062092 INSPEC Abstract Number: B9202-6210-155, C9202-7410F-136

Title: Estelle-based test generation tool

Author(s): Sarikaya, B.; Forghani, B.; Eswara, S.

Author Affiliation: Dept. of Comput. & Inf. Sci., Bilkent Univ., Ankara, Turkey

Journal: Computer Communications vol.14, no.9 p.534-44

Publication Date: Nov. 1991 Country of Publication: UK

CODEN: COCOD7 ISSN: 0140-3664

U.S. Copyright Clearance Center Code: 0140-3664/91/009534-11\$3.00

Language: English

Subfile: B C

Abstract: A **test** design tool (Contest-Estl) for functional **analysis** and **test** derivation of protocols formally specified using an extended finite-state machine model is presented. The formal description...

... s main components include a compiler, a normalizer, a multiple module transition tour generator and several interactive **programs**. The tool is based on a static **analysis** of Estelle called normalization, which is explained in detail with various examples. The normalized specification

facilitates graphical...

... of the control and data flow in the specification by the interactive tools. Next the authors discuss **test** generation, which is based on **verifying** the control and **data flow**. First the data flow graph must be decomposed into **blocks** where each **block** represents the data flow in a protocol function. From the control graph the tool generates transition tours, and then **test** sequences are derived from the transition tour to **test** each function. The **performance** of the tool on various applications is also presented.

27/3,K/13 (Item 13 from file: 2)

DIALOG(R)File 2:INSPEC

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03787698 INSPEC Abstract Number: A91008236

Title: Design and R&D for the central solenoid for the Compact Ignition Tokamak (CIT)

Author(s): Thome, R.J.; Pillsbury, R.D., Jr.; Smith, B.A.; Vieira, R.; Thomas, P.; Olmstead, M.; Mann, W.R.; Winn, P.; Titus, P.; Myatt, L.

Author Affiliation: Plasma Fusion Center, MIT, Cambridge, MA, USA

Conference Title: Proceedings, IEEE Thirteenth Symposium on Fusion Engineering (Cat. No.89CH2820-9) p.769-72 vol.2

Editor(s): Lubell, M.S.; Nestor, M.B.; Vaughan, S.F.

Publisher: IEEE, New York, NY, USA

Publication Date: 1990 Country of Publication: USA 2 vol. 1569 pp.

U.S. Copyright Clearance Center Code: CH2820-9/89/0000-0769\$1.00

Conference Sponsor: IEEE; AIAA; ANS; DOE; Oak Ridge Nat. Lab.; Bechtel Int.; McDonnell Douglas Astronautics Co.; TRW; Westinghouse Electr. Corp

Conference Date: 2-6 Oct. 1989 Conference Location: Knoxville, TN, USA

Language: English

Subfile: A

...Abstract: 8 m, a height of 4.8 m, and a central field of 23-256 T. Geometric **restrictions** are such that an aggressive structure concept is required. Two options are being considered for the machine...

... 718 ratio of 50/50. Each plate is cut into eight turns by a water-jet cutting **process**. Features of this design are presented along with selected results from an R&D **program** under way. Full-scale plates have been fabricated and cut, and prototypical mechanical and electrical joints have...

...central solenoid to receive partial support from the TF (toroidal field) coil system. In this case the **operating** scenario and stresses are substantially different. R&D tasks have been started to evaluate conductor lifetime under simulated **operating** conditions for alumina dispersion strengthened (ADS) copper. Explosively bonded copper/Alloy-718 plates remain an option, but with a substantial increase in the ratio of copper to Alloy 718 to 70/30. The **status** of **test** results and plans for these materials is given.

27/3,K/14 (Item 14 from file: 2)

DIALOG(R)File 2:INSPEC

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03568713 INSPEC Abstract Number: B90015367

Title: A 500 MHz 16*16 complex multiplier using self-aligned gate heterostructure FET technology

Author(s): Akinwande, T.; MacTaggart, R.; Betz, K.; Grider, D.; Nohava, T.; Nohava, J.; Lange, T.; Tetzlaff, D.; Arch, D.

Author Affiliation: Honeywell Sensors & Signal Process. Lab., Bloomington, MN, USA

Conference Title: 1989 IEEE International Solid-State Circuits Conference. Digest of Technical Papers. 36th ISSCC. First Edition (Cat. No.CH2684-9) p.232-3, 347

Publisher: IEEE, New York, NY, USA

Publication Date: 1989 Country of Publication: USA 394 pp.

U.S. Copyright Clearance Center Code: 0193-6530/89/0000-0232\$01.00
Conference Sponsor: IEEE; Univ. Pennsylvania
Conference Date: 15-17 Feb. 1989 Conference Location: New York, NY,
USA
Language: English
Subfile: B

...Abstract: modified Booth's algorithm to reduce the number of adders in the multiplier net. The basic building **block** of the adders and registers is a DCFL NOR gate with a special load structure. The heterostructure...

... 3-in semi-insulating LEC (liquid encapsulated Czochralski) GaAs substrates. The output of bits 1 and 5, **operating** in the self- **test mode** , is shown. The circuit was found to **perform** vector rotations correctly at a clock rate of 560 MHz with a power dissipation of 6.2...

... 96 ps/gate and 0.89 mW/gate because the pipelined stages are limited to 20 gate **delays** . This **performance** represents a loaded **delay** with each gate driving an average fan-out of 2.5 and about 1000 mu m of...

27/3,K/15 (Item 15 from file: 2)
DIALOG(R)File 2:INSPEC
(c) 2004 Institution of Electrical Engineers. All rts. reserv.

03297799 INSPEC Abstract Number: C89014706

Title: A solution to a problem with Morel and Renvoise's 'Global optimization by suppression of partial redundancies'

Author(s): Drechsler, K.H.; Stadel, M.P.

Author Affiliation: Siemens AG, Munchen, West Germany

Journal: ACM Transactions on Programming Languages and Systems vol.10,
no.4 p.635-40

Publication Date: Oct. 1988 Country of Publication: USA

CODEN: ATPSDT ISSN: 0164-0925

Language: English

Subfile: C

...Abstract: redundancies ((Commun. ACDM, vol.22, no.2, p.96-103, 111-26, 1979)). Morel and Renvoise use **data flow analysis** to **determine** expression computations that should be inserted at the end of certain basic **blocks** and to determine redundant computations that can be eliminated. The **execution** of these techniques results in the movement of loop invariant expressions out of the loop. In addition...

27/3,K/16 (Item 16 from file: 2)
DIALOG(R)File 2:INSPEC
(c) 2004 Institution of Electrical Engineers. All rts. reserv.

03088725 INSPEC Abstract Number: C88019645

Title: A performance improvement expert system for ACOS-4 operating system

Author(s): Asogawa, M.; Shiramizu, A.; Ikumi, K.; Kubo, H.

Author Affiliation: C&C Syst. Res. Labs., NEC Corp., Tokyo, Japan

Journal: NEC Research and Development no.87 p.86-94

Publication Date: Oct. 1987 Country of Publication: Japan

CODEN: NECRAU ISSN: 0547-051X

Language: English

Subfile: C

Abstract: Presents a prototype **performance** improvement expert system for the ACOS-4 **operating** system, using the expert system construction tool EXCORE developed by NEC. The system reads system **status** information (SMF), **performs analysis** to determine the true cause of the **performance** problem, and gives the user advice for the solution. In this system, the expert's knowledge is...

... into the following three groups. Rules in the initial interview group list up all suspected causes of **performance** problems by consulting with

system engineers; rules in the diagnosis group diagnose the suspected causes of **performance** problem presented by the initial interview **block** ; and rules in the counterplan group indicate specific counterplans for the causes of **performance** problems indicated by the diagnostic **block** . In each group, knowledge is divided into layers. By using these structured expressions, the expert's knowledge...

27/3,K/17 (Item 17 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2004 Institution of Electrical Engineers. All rts. reserv.

02939643 INSPEC Abstract Number: A87100651

Title: Nonresonant multiphoton ionization of the naturals ablated in laser microprobe mass spectrometry analysis of GaAs and Hg/sub 0.78/Cd/sub 0.22/Te

Author(s): Schueler, B.; Odom, R.W.

Author Affiliation: Charles Evans & Associates, Redwood City, CA, USA

Journal: Journal of Applied Physics vol.61, no.9 p.4652-61

Publication Date: 1 May 1987 Country of Publication: USA

CODEN: JAPIAU ISSN: 0021-8979

U.S. Copyright Clearance Center Code: 0021-8979/87/094652-10\$02.40

Language: English

Subfile: A

...Abstract: laser of this microanalytical materials analysis technique. The irradiance of this focused postablation ionization (PAI) laser beam **operating** at 266 nm is sufficient to ionize all the neutral species having an ionization potential less than 9.3 eV that are contained within the irradiated volume via two photon nonresonant multiphoton ionization **processes** . This laser ablation/laser ionization configuration of the laser microprobe technique has been applied to the microanalysis of high-purity GaAs and Hg/sub 0.78/Cd/sub 0.22/Te target materials. The **elemental** ion yields produced in the PAI microanalysis of these materials are generally larger than those produced by the laser ablation only **mode** of laser microprobe **analysis** . The **elemental** ion yields of the constituents in these materials have also been evaluated as a function of the **delay** time between the firing of ablation and PAI lasers at several ablation laser irradiances.

27/3,K/18 (Item 18 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2004 Institution of Electrical Engineers. All rts. reserv.

02937964 INSPEC Abstract Number: C87048617

Title: P-Prolog: a parallel logic language based on exclusive relation

Author(s): Yang, R.; Aiso, H.

Author Affiliation: Dept. of Electr. Eng., Keio Univ., Yokohama, Japan

Journal: New Generation Computing vol.5, no.1 p.79-95

Publication Date: 1987 Country of Publication: Japan

CODEN: NGCOE5 ISSN: 0288-3635

Conference Title: Third International Conference on Logic Programming

Conference Date: July 1986 Conference Location: London, UK

Language: English

Subfile: C

...Abstract: or-parallelism. Compared with the other parallel logic programming languages, syntactic constructs such as read-only annotation, **mode** declaration and communication constraints are not used in P-Prolog. A new concept introduced in P-Prolog...

... exclusive relation of guarded Horn clauses. Advances included in P-Prolog are: (1) the synchronization mechanism can **determine** the direction of **data flow** dynamically; and (2) guarded Horn clauses can be interpreted as either don't care non-determinism or...

27/3,K/19 (Item 19 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2004 Institution of Electrical Engineers. All rts. reserv.

02698801 INSPEC Abstract Number: C86039025

Title: Performance evaluation of a simulated data-flow computer with low-resolution actors

Author(s): Gaudiot, J.L.; Ercegovac, M.D.

Author Affiliation: Comput. Res. Inst., Univ. of Southern California, Los Angeles, CA, USA

Journal: Journal of Parallel and Distributed Computing vol.2, no.4
p.G544-86001

Publication Date: Nov. 1985 Country of Publication: USA

CODEN: JPDCEP ISSN: 0743-7315

U.S. Copyright Clearance Center Code: 0743-7315/85\$3.00

Language: English

Subfile: C

...Abstract: which the level of primitives can be selected so that the overhead due to the data-flow **mode** of operation is reduced. A **deterministic** simulation of a **data - flow** machine with a variable number of processing elements was undertaken and is described. The tests were performed...

27/3,K/20 (Item 20 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2004 Institution of Electrical Engineers. All rts. reserv.

02596236 INSPEC Abstract Number: B86008084, C86008645

Title: VLSI architectures for real-time image processing

Author(s): Narendra, P.

Author Affiliation: Honeywell Inc., Colorado Springs, CO, USA

Conference Title: Topical Meeting on Machine Vision p.FD4/1

Publisher: Opt. Soc. America, Washington, DC, USA

Publication Date: 1985 Country of Publication: USA 185 pp.

Conference Sponsor: Opt. Soc. America; IEEE; SPIE

Conference Date: 20-22 March 1985 Conference Location: Incline Village, NV, USA

Language: English

Subfile: B C

Abstract: Summary form only given, substantially as follow. The author summarizes typical sensor formats and algorithms to **identify** common **data flow** and computational structures required for their real-time VLSI implementation. A set of criteria for **evaluating** architectures for VLSI is developed and used to examine several representative architectures. Recommendations for common VLSI building **blocks** are made for the representative architectures.

27/3,K/21 (Item 21 from file: 2)

DIALOG(R)File 2:INSPEC

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01929014 INSPEC Abstract Number: C82039207

Title: Jackson structured programming in an on-line environment

Author(s): Jansen, H.

Journal: Informatie vol.24, no.6 p.341-8

Publication Date: June 1982 Country of Publication: Netherlands

CODEN: INFTCR ISSN: 0019-9907

Language: Dutch

Subfile: C

...Abstract: approach to the development of on-line programs is discussed with the emphasis on the need to **identify** the 'serial **data flow**', and the consequences of the conversational **mode** for program design and implementation. With the JSP method, design and implementation are always

clearly separated; the...

27/3,K/22 (Item 22 from file: 2)
DIALOG(R)File 2:INSPEC
(c) 2004 Institution of Electrical Engineers. All rts. reserv.

01046176 INSPEC Abstract Number: C77012859

Title: Performance analysis of a data-flow processor
Author(s): Misunas, D.P.
Author Affiliation: Lab. for Computer Sci., MIT, Cambridge, MA, USA
Conference Title: Proceedings of the 1976 International Conference on Parallel Processing p.100-4
Publisher: IEEE, New York, NY, USA
Publication Date: 1976 Country of Publication: USA xi+328 pp.
Conference Sponsor: Wayne State Univ.; IEEE; ACM
Conference Date: 24-27 Aug. 1976 Conference Location: Walden Woods, MI, USA
Language: English
Subfile: C

Abstract: A data flow processor is structured as a packet communication system. Sections of a processor are connected by interconnection networks which have a great deal of inherent parallelism, and the sections communicate by means of fixed size information packets. The processing capability of a data - flow processor is determined through consideration of the flow of packets within the interconnection networks, and the actual performance of the processor is affected by the structure of the networks. The execution time of an instruction in a processor can vary greatly due to conflict within the interconnection networks. The performance of a data-flow processor is measured through consideration of the delays caused by this conflict, and the proper network structure and processing rate of a machine are determined through analysis of the best and worst case delays.

27/3,K/23 (Item 1 from file: 6)
DIALOG(R)File 6:NTIS
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2107315 NTIS Accession Number: DE98007251/XAB

Authenticated tracking and monitoring system (ATMS) tracking shipments from an Australian uranium mine

Schoeneman, J. L.
USDOE, Washington, DC.
Corp. Source Codes: 8888888888
Report No.: SAND-98-1767C
31 Dec 98 9p
Languages: English Document Type: Conference proceeding
Journal Announcement: GRAI9907; ERA9901
Institute of Nuclear Materials Management (INMM) meeting.
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NTIS Prices: PC A02/MF A01

... uses wireless sensor packs to monitor the status of the items and environmental conditions. A receiver and processing unit collect a variety of sensor event data. The collected data are transmitted to the INMARSAT satellite communication system, which then sends the data...
... safety and security of a number of items in transit along a scheduled shipping route. The resulting tracking, timing, and status information could then be processed to ensure compliance with various agreements. Following discussions between the Australian Safeguards Office (ASO), the US Department...

... a field trial prototype ATMS to track and monitor shipments of uranium

ore concentrate (UOC) from an **operating** uranium mine in Australia to a final destination in Rotterdam, the Netherlands, with numerous **stops** along the way. During the months of February and March 1998, the trial was conducted on a...

27/3,K/24 (Item 2 from file: 6)
DIALOG(R)File 6:NTIS
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2082896 NTIS Accession Number: SAE AIR 1939/XAB

Aircraft Engine Life Cycle Cost Guide. (SAE Standard)

Society of Automotive Engineers, Inc., Warrendale, PA.

Corp. Source Codes: 058588000

cDec 86 42p

Languages: English

Journal Announcement: GRAI9818

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NTIS Prices: PC\$70.50

... Cost (LCC) analysis for military aircraft engines. AIR 1939 deals specifically with the direct Life Cycle Cost **elements** of Aerospace Propulsion Systems. normally one part of a larger system, and that through its **performance** interaction with this overall system, propulsion can often exert a greater influence on system costs than are possible through changes in the direct propulsion system cost **elements**. this **performance** interaction issue. Cost approach which is a stand-alone measure of propulsion system cost - but which can also serve as a cost building **block** for broader system-oriented cost-benefit evaluations. communication of LCC data between equipment suppliers, aircraft engine producers...

... and users. addressed include research, development, test and evaluation (RDT&E); acquisition (initial procurement and investment); and **operating** and support (O&S) costs. calculation procedures and cost methodology are specifically included since many LCC models...

... provides a basis for LCC calculations, for monitoring of LCC status resulting from trade studies, and for **tracking status** against LCC goals in a consistent manner, thus permitting quantitative, comparable assessment. presentation include consideration of overall cost rather than a sub-optimization of just one or a few cost **elements**.

27/3,K/25 (Item 3 from file: 6)
DIALOG(R)File 6:NTIS
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1808046 NTIS Accession Number: AD-A278 898/2

Symbolic Debugging of Globally Optimized Code: Data Value Problems and Their Solutions

(Research rept)

Adl-Tabatabai, A. R. ; Gross, T.

Carnegie-Mellon Univ., Pittsburgh, PA. Dept. of Computer Science.

Corp. Source Codes: 005343001; 403081

Report No.: CMU-CS-94-105

Jan 94 21p

Languages: English

Journal Announcement: GRAI9416

Order this product from NTIS by: phone at 1-800-553-NTIS (U.S. customers); (703)605-6000 (other countries); fax at (703)321-8547; and email at orders@ntis.fedworld.gov. NTIS is located at 5285 Port Royal Road, Springfield, VA, 22161, USA.

Symbolic debuggers are **program** development tools that allow a user to interact with an **executing process** at the source level. In response to a user query, the debugger must be able to retrieve...
... variable in a manner consistent with what the user expects with respect to the source statement where **execution** has **halted**. However, when a **program** has been compiled with optimizations, values of variables may either be inaccessible in the run-time state...

... the information a symbolic debugger can provide a user when data value problems occur. We provide a **data flow** algorithm that **detects** the impact of two global transformations: code hoisting and dead code elimination. These two transformations capture the...

... in terms of these transformations. Symbolic debugging, Endangered variables, Suspect variables, Noncurrent variables, Compiler optimization, Data flow **analysis**.

27/3,K/26 (Item 4 from file: 6)
DIALOG(R)File 6:NTIS
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1796484 NTIS Accession Number: DE94002465

**Travel to Europe to collaborate on wind turbine aerodynamics and testing.
Foreign trip report, September 15--23, 1993**

Simms, D. A.
National Renewable Energy Lab., Golden, CO.
Corp. Source Codes: 102636000; 9529807
Sponsor: Department of Energy, Washington, DC.
Report No.: DOE/FTR-94002465
15 Oct 93 7p
Languages: English Document Type: Conference proceeding
Journal Announcement: GRAI9412; ERA9420
Sponsored by Department of Energy, Washington, DC.
U.S. Sales Only. Order this product from NTIS by: phone at 1-800-553-NTIS (U.S. customers); (703)605-6000 (other countries); fax at (703)321-8547; and email at orders@ntis.fedworld.gov. NTIS is located at 5285 Port Royal Road, Springfield, VA, 22161, USA.

NTIS Prices: PC A02/MF A01

... Denmark. Topics discussed at this meeting of the newly-formed Annex centered around planning and coordinating research **objectives** and activities between all of the participants. There are 5 participating organizations from 4 countries, and each will be conducting detailed aerodynamic test **programs** to investigate steady and unsteady aerodynamic behavior on full-scaled wind turbines. The participants gave presentations describing their **test** plans and the **status** of their facilities. Detailed discussions were then held to finalize the formats of the measurement **programs** and information exchange. Initial sample data sets were prepared and exchanged by those participants who already had...

... meeting and site visit was conducted at the ECN facility near Petten, Netherlands. ECN is in the **process** of checking out their aerodynamic pressure force measurement system on a non-rotating outdoor test facility. The 12.5 m blade has been instrumented and installed on a test stand which **holds** the blade horizontally for work access, and tilts it vertically if sufficient winds are available to generate aerodynamics forces to test the measurement system. ECN's test plan has been **delayed** due to rain and low winds all summer. Once these tests are completed and they are sure the system is **operating** as required, they plan to install the blade on their 25 m custom test-bed turbine. On...

... Imperial College Test Facility in the UK. They have completed wind tunnel testing and are in the **process** of building a ruggedized pressure measurement system and refurbishing the Wind Harvester test turbine on which the...

27/3,K/27 (Item 5 from file: 6)
DIALOG(R)File 6:NTIS
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1002272 NTIS Accession Number: AD-A121 970/8

Development of an Automated Gear Tooth Contour Measuring Device

(Final rept. on Phase 1)

Ouellette, A. R.

Aerodyne Research, Inc., Billerica, MA.

Corp. Source Codes: 076227000; 390112

12 Apr 82 112p

Languages: English

Journal Announcement: GRAI8307

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NTIS Prices: PC A06/MF A01

... minus 30 microinches using a noncontacting optical probe. The Phase I testing demonstrated state-of-the-art **performance** as an environmentally compensated coordinate measuring machine wherein precision and absolute accuracy was shown to be better...

... noncontacting optic probe, also a state-of-the-art development, showed the sensitivity needed to meet the **program** goals. For the x-y coordinate measuring machine, errors associated with scale accuracy, vibration, structural rigidity, geometry...

... thermal effects were first defined analytically, and then correlated with the test data. Thermal stability tests were **performed** to show effects of machine warm-up from a cold start, effects of ambient temperature and atmospheric pressure changes, effects of human body heat, stabilized and transient **operating** conditions and the benefits of fluid filling the machine structure. Repeatability tests were done for simple mechanical positioning precision and for part mensuration in the environmentally compensated **mode**. To **evaluate** absolute accuracy **performance** as an environmentally compensated system, gauge **blocks** were measured in an uncontrolled room at varying temperature around 12 degrees F higher than the meteorological...

27/3,K/28 (Item 6 from file: 6)
DIALOG(R)File 6:NTIS
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0775380 NTIS Accession Number: N79-27237/3/XAB

Handbook on Astronaut Crew Motion Disturbances for Control System Design

Kullas, M. C.

Martin Marietta Aerospace, Denver, CO.

Report No.: NASA-RP-1025; L-12299

May 79 196p

Languages: English

Journal Announcement: GRAI7922; STAR1718

Order this product from NTIS by: phone at 1-800-553-NTIS (U.S. customers); (703)605-6000 (other countries); fax at (703)321-8547; and email at orders@ntis.fedworld.gov. NTIS is located at 5285 Port Royal Road, Springfield, VA, 22161, USA.

NTIS Prices: PC A09/MF A01

The **analyses** and results pertinent to the characterization of the disturbances imparted to the Skylab vehicle by the T...

...013 characterizations outlined, begin with the control system conceptual design and conclude with preliminary expectations for pointing **performance** as affected by crew motions. **Block** diagrams to highlight the contents so

that the reader can easily **identify** the information and **data flow** are used. These diagrams provide a handy cross reference of related topics.

27/3,K/29 (Item 1 from file: 8)
DIALOG(R)File 8:EI Compendex(R)
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04967726 E.I. No: EIP98034111111

Title: Controlling glass furnace NO//x with gas reburn

Author: Koppang, Richard; Marquez, Antonio; Moyeda, David; Joshi, Michael
; Mohr, Patrick; Madrazo, Roger
Corporate Source: Energy and Environmental Research Corp, Irvine, CA, USA
Source: American Ceramic Society Bulletin v 77 n 2 Feb 1998. p 51-56
Publication Year: 1998
CODEN: ACSBA7 ISSN: 0002-7812
Language: English

Abstract: The **objective** of the development and demonstration **program** is to commercialize gas reburn (GR) technology for controlling NO//x emissions from a sideport, regenerated glass furnace. One furnace was modified to implement full gas reburning with the **objective** up to 70% control in the GR **operating mode**. The **test and evaluation** phase of the demonstration was **performed** in three discrete test **blocks**: furnace optimization for as-found and GR operation; GR startup through serial commissioning of port pairs; parametric **performance** tests at progressively higher GR flows followed by long-term operation at an optimized condition.

27/3,K/30 (Item 2 from file: 8)
DIALOG(R)File 8:EI Compendex(R)
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02878424 E.I. Monthly No: EIM9003-013966

Title: 500 MHz 16 multiplied by 16 complex multiplier using self-aligned gate heterostructure FET technology.

Author: Akinwande, Tayo; MacTaggart, Ross; Betz, Keith; Grider, David; Nohava, Thomas; Nohava, James; Lange, Thomas; Tetzlaff, David; Arch, David
Corporate Source: Honeywell Sensors & Signal Process Lab, Bloomington, MN, USA

Conference Title: IEEE International Solid-State Circuits Conference Digest of Technical Papers (ISSCC 1989)

Conference Location: New York, NY, USA Conference Date: 19890215

E.I. Conference No.: 12816

Source: Digest of Technical Papers - IEEE International Solid-State Circuits Conference v 32. Publ by IEEE, IEEE Service Center, Piscataway, NJ, USA. Available from IEEE Service Cent (cat n 89CH2684-9), Piscataway, NJ, USA. p 232-233, 347

Publication Year: 1989

CODEN: DTPCDE

Language: English

...Abstract: modified Booth's algorithm to reduce the number of adders in the multiplier net. The basic building **block** of the adders and registers is a DCFL NOR gate with a special load structure. The heterostructure...

...3-in semi-insulating LEC (liquid encapsulated Czochralski) GaAs substrates. The output of bits 1 and 5, **operating** in the self- **test mode**, is shown. The circuit was found to **perform** vector rotations correctly at a clock rate of 560 MHz with a power dissipation of 6.2...

...96 ps/gate and 0.89 mw/gate because the pipelined stages are limited to 20 gate **delays**. This **performance** represents a loaded **delay** with each gate driving an average fan-out of 2.5 and about 1000 mu m of...

27/3,K/31 (Item 3 from file: 8)

DIALOG(R)File 8:EI Compendex(R)
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02056564 E.I. Monthly No: EI8612119496 E.I. Yearly No: EI86023808
Title: AUTOMATED TESTING IN A SOFTWARE TEST BENCH ENVIRONMENT.
Author: Tissue, Eric
Corporate Source: Syscon Corp
Source: ITEA J Test Eval v 7 n 2 Spring 1986 p 32-36
Publication Year: 1986
CODEN: IJTEEQ
Language: ENGLISH

Abstract: The **Event** Specification Language (ESL) was developed by the Westinghouse Defense and Electronics Center to automate the test and evaluation of Embedded Computer Operational Control **Programs** (OCP's). ESL is a **block** structured **programming** language which operates on a Software Test Bench facility with an environment simulator to define, examine, and modify the simulated environment. By allowing test cases to be **programmed** and entered in batch **mode** with **test** decisions made a priori, ESL eliminates the need for operator intervention during test **execution** and ensures repeatability of the test. This paper describes the functional capabilities, **operating** environment and applications of ESL. In addition, it describes a test methodology that incorporates ESL and presents...

27/3,K/32 (Item 4 from file: 8)
DIALOG(R)File 8:EI Compendex(R)
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01354787 E.I. Monthly No: EI8305032636 E.I. Yearly No: EI83017666
Title: Set of Tools for Computer-aided Design of High Performance Digital Systems.
Title: ENSEMBLE D'OUTILS POUR LA CONCEPTION ASSISTEE PAR ORDINATEUR DE SYSTEMES NUMERIQUES A HAUTE PERFORMANCE.
Author: Michel, Claude
Corporate Source: Soc SINTRA
Source: RGE, Revue Generale de l'Electricite n 6 Jun 1982, Journ d'Etud SEE La Surete de Fonct dans les Syst Ind Autom, Gif-sur-Yvette, Fr, Jun 4 1981 P 441-444
Publication Year: 1982
CODEN: RGELAC ISSN: 0035-3116
Language: FRENCH

Abstract: After recalling briefly the Petri net method, a homogeneous set of **programs** is introduced. It constitutes a chain of tools for Computer Aided Design (CAD) of complex logical systems used in signal **processing**. Three **programs** have been developed with the assistance of the Universities of Grenoble, Nice and Paris and sponsored by DRET. The **program**, Simulation of Petri Nets, permits description of specifications in a precise and unambiguous manner and **evaluation** of the characteristics of this model (absence of **blockage**, etc.). Based on this model, two other **programs** give hardware and software implementations, with specific constraints of implementation. The **program**, implementation of specialized operators, allows optimization of **data flow** and **determines** sequencing according to the resources allowed. Definition of PROM and FPLA implementing this sequencing becomes feasible. The software implementation **program** assures translation of Petri nets into the **programming** language. In French.

27/3,K/33 (Item 1 from file: 34)
DIALOG(R)File 34:SciSearch(R) Cited Ref Sci
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04965367 Genuine Article#: UV349 No. References: 21
Title: EVALUATION OF EXTENDED LIGHT PATH CAPILLARIES FOR USE IN CAPILLARY ELECTROPHORESIS WITH LASER-INDUCED FLUORESCENCE DETECTION
Author(s): COLE RO; HILLER DL; CHWOJDAK CA; SEPANIAK MJ

Corporate Source: PFIZER INC, DEPT DRUG METAB/GROTON//CT/06340; UNIV
TENNESSEE, DEPT CHEM/KNOXVILLE//TN/37996
Journal: JOURNAL OF CHROMATOGRAPHY A, 1996, V736, N1-2 (JUN 7), P239-245
ISSN: 0021-9673
Language: ENGLISH Document Type: ARTICLE (Abstract Available)

...Abstract: sensitivity is the Hewlett-Packard extended light path capillary (ELP). These capillaries have a 'bubble' in the **flow path** at the **detection** zone which provides an increased optical path length. Although well characterized for use in absorbance detection, they have not been adequately assessed in the laser-induced fluorescence (LIF) detection **mode**. Similar advantages (e.g., improved detectability) are expected with LIF, in addition to simplifying the optical alignment...

27/3,K/34 (Item 2 from file: 34)
DIALOG(R) File 34:SciSearch(R) Cited Ref Sci
(c) 2004 Inst for Sci Info. All rts. reserv.

02577101 Genuine Article#: LM892 No. References: 52
Title: IMPROVED EXERCISE TEST ACCURACY USING DISCRIMINANT FUNCTION-ANALYSIS AND RECOVERY ST SLOPE
Author(s): RODRIGUEZ M; MOUSSA I; FRONING J; KOCHUMIAN M; FROELICHER VF
Corporate Source: LONG BEACH VET AFFAIRS MED CTR, DEPT CARDIOL, DIVCARDIOL
IIIC, 5901 E 17TH ST/LONG BEACH//CA/90822
Journal: JOURNAL OF ELECTROCARDIOLOGY, 1993, V26, N3 (JUL), P207-218
ISSN: 0022-0736
Language: ENGLISH Document Type: ARTICLE (Abstract Available)

Abstract: The **objective** of the study was to optimize the accuracy of the exercise test for predicting the presence of...

...stored digital exercise electrocardiographic data on 147 men who had undergone exercise testing and cardiac catheterization was **performed**. With significant coronary artery disease defined as less-than-or-equal-to 70% stenosis, 95 patients had...

...receiving digoxin, had a myocardial infarction or previous coronary artery bypass graft, or exhibited left bundle branch **block**, left ventricular hypertrophy, Q waves, or ST depression on their resting electrocardiogram. Analysis was **performed** using the authors' averaging and measurement software at rest and at each 30 seconds throughout the exercise...

...scores. The setting was a 1,000 bed Veterans Affairs Medical Center (Long Beach, CA). Discriminant function **analysis** chose age, smoking **status**, presenting chest pain characteristics, and lead V5 ST slope in recovery to have independent power for separating...

...coronary artery disease. A discriminant function score using these four variables was used to form a receiver **operating** characteristics curve (and derive receiver **operating** characteristics curve areas) for comparison to other exercise test methods and scores: (discriminant function score = .81; slope...

...method/delta heart rate) = .75; Hollenberg score = .68; Hollenberg areas only = .66; and ST integral = .66. Receiver **operating** characteristics curve analysis revealed a trend for the discriminant function score to be superior to all other measurements and scores. Recovery ST slope in lead V5 **performed** as well as or better than all other electrocardiographic criteria or treadmill scores except for the authors...

27/3,K/35 (Item 3 from file: 34)
DIALOG(R) File 34:SciSearch(R) Cited Ref Sci
(c) 2004 Inst for Sci Info. All rts. reserv.

01764296 Genuine Article#: HZ136 No. References: 25

Title: AGING IN HIGHER FUNCTIONING ADULTS WITH DOWNS-SYNDROME - AN INTERIM-REPORT IN A LONGITUDINAL-STUDY

Author(s): DEVENNY DA; HILL AL; PATXOT O; SILVERMAN WP; WISNIEWSKI KE

Corporate Source: NEW YORK STATE INST BASIC RES DEV DISABILITIES, 1050 FOREST HILL RD/STATEN ISL//NY/10314

Journal: JOURNAL OF INTELLECTUAL DISABILITY RESEARCH, 1992, V36, JUN (JUN), P241-250

Language: ENGLISH Document Type: ARTICLE (Abstract Available)

...Abstract: 55 years were compared to 18 subjects without DS who were of similar IQ and age. The **evaluation** of mental **status** consisted of **testing** orientation to person, place and time, **object** naming, visuomotor coordination, and concentration. Auditory memory was tested with an adapted version of the Buschke Memory test. Visual memory testing consisted of matching shapes which were presented simultaneously and after **delays** of 0, 5 and 10s on a computer-controlled screen. No changes were found in test scores between an initial testing and follow-up testing up to 5 years later, indicating that ageing **processes** were not having a major impact in the cognitive **functioning** of these subjects. Furthermore, there was no indication of any generalized decline in **performance** suggestive of early symptoms of dementia among the older subjects with DS.

27/3,K/36 (Item 1 from file: 35)

DIALOG(R)File 35:Dissertation Abs Online

(c) 2004 ProQuest Info&Learning. All rts. reserv.

01614124 ORDER NO: AAD98-12240

AUTOMATED STUDENT PERFORMANCE MONITORING IN TRAINING SIMULATION (ARTIFICIAL INTELLIGENCE, FLIGHT SIMULATION)

Author: DREWES, PETER

Degree: PH.D.

Year: 1997

Corporate Source/Institution: UNIVERSITY OF CENTRAL FLORIDA (0705)

Source: VOLUME 58/10-B OF DISSERTATION ABSTRACTS INTERNATIONAL.

PAGE 5624. 304 PAGES

...in the real-time flight simulation environment. A physical flight simulator consisting of a heads-down display, **operating** controls and switches and actual flight coordinates was constructed and software developed for use in the student monitoring **process**.

A baseline artificial intelligence technique, based on case-based reasoning and entitled Case-based Reasoning Automated Monitoring...

27/3,K/37 (Item 2 from file: 35)

DIALOG(R)File 35:Dissertation Abs Online

(c) 2004 ProQuest Info&Learning. All rts. reserv.

01535359 ORDER NO: AAD97-08499

COGNITION AS A PREDICTOR OF REHABILITATION OUTCOME IN OLDER AMPUTEES (ELDERLY)

Author: BARNFIELD, KAY

Degree: PSY.D.

Year: 1996

Corporate Source/Institution: SPALDING UNIVERSITY (0965)

Source: VOLUME 57/10-B OF DISSERTATION ABSTRACTS INTERNATIONAL.

PAGE 6555. 125 PAGES

...Independence Measure) was defined as the degree to which a patient was able to ambulate and to **perform** basic self-care activities without the assistance of others. Secondary affective, demographic, and medical predictor variables known to affect either cognitive status or functional independence in older persons were also studied. Stepwise multiple regression **analyses** determined cognitive **status** (as measured by the

Dementia Rating Scale and the Wechsler Adult Intelligence Test-Revised **Block** Design subtest) and level of depression (as measured by the Geriatric Depression Scale) together to be the...

...for 68% and 69%, respectively, of the variance in level of functional independence at discharge. Higher cognitive **functioning** and lower levels of depression were strongly correlated ($r=.59$ and $r=-.58$; $p<.001$) with higher levels of functional. Subjects who completed the entire neurocognitive battery ($n=8$) evidenced significantly higher levels of cognitive **functioning** ($p<.004$) and shorter lengths of hospital stay ($p<.04$). Differential **performance** on the Trail Making Test, Part B seemed to identify two older amputee subgroups (i.e., Blacks...

27/3,K/38 (Item 3 from file: 35)
DIALOG(R)File 35:Dissertation Abs Online
(c) 2004 ProQuest Info&Learning. All rts. reserv.

01286180 ORDER NO: NOT AVAILABLE FROM UNIVERSITY MICROFILMS INT'L.

THE NEUROPSYCHOLOGY OF ALCOHOL ABUSE: AN EXPLORATORY STUDY

Author: ALDERDICE, FIONA ALISON

Degree: PH.D.

Year: 1991

Corporate Source/Institution: QUEEN'S UNIVERSITY OF BELFAST (NORTHERN IRELAND) (0725)

Source: VOLUME 54/02-C OF DISSERTATION ABSTRACTS INTERNATIONAL.
PAGE 638. 313 PAGES

Location of Reference Copy: THE QUEEN'S UNIVERSITY OF BELFAST, SCIENCE LIBRARY, CHLORINE GARDENS, BELFAST BT9 5AG, NORTHERN IRELAND

...to problem drinkers with Organic Brain Damage. The final test battery included the Adult Memory and Information **Processing** Battery (Coughlan & Hollows 1985), Recency Discrimination and Recognition Tasks adapted from Milner (1981), **Block** Design subtest from the WAIS (Wechsler 1975) and measures of premorbid IQ. Measures of personality and subjective ...

...were obtained and a 5ml clotted sample was drawn from each subject to obtain indicators of liver **functioning** and mineral **status**. Factor **analysis** was carried out on the neuropsychological variables to reduce the data and to examine the construct validity...

27/3,K/39 (Item 4 from file: 35)
DIALOG(R)File 35:Dissertation Abs Online
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01086360 ORDER NO: NOT AVAILABLE FROM UNIVERSITY MICROFILMS INT'L.

STATIC ANALYSIS OF PROLOG PROGRAMS

Original Title: STATISCHE ANALYSE VON PROLOG-PROGRAMMEN

Author: REICHL, FRANZ

Degree: DR.TECHN.

Year: 1988

Corporate Source/Institution: TECHNISCHE UNIVERSITAET WIEN (AUSTRIA) (5807)

Source: VOLUME 51/01-C OF DISSERTATION ABSTRACTS INTERNATIONAL.
PAGE 132. 167 PAGES

Location of Reference Copy: TECHNICAL UNIVERSITY OF VIENNA, A-1040 VIENNA, AUSTRIA

...The methods and tools are: (1) type analysis (both narrowing and generation of every possible type), (2) **mode analysis** to find out which arguments are used as input or output parameters, (3) symbolic evaluation **executing** the program in a symbolic domain to find out properties about data and program behavior, (4) program and **data flow** analysis to **determine** termination and endless loops due to recursions, and (5) special

methods such as cross reference lists, spelling...

27/3,K/40 (Item 5 from file: 35)
DIALOG(R)File 35:Dissertation Abs Online
(c) 2004 ProQuest Info&Learning. All rts. reserv.

0956333 ORDER NO: AAD87-12308

SOFTWARE INTERFACE FOR MULTIPROCESSOR SIMULATION

Author: MAKOUI, ALI

Degree: PH.D.

Year: 1986

Corporate Source/Institution: UNIVERSITY OF CALIFORNIA, LOS ANGELES (0031)

Source: VOLUME 48/03-B OF DISSERTATION ABSTRACTS INTERNATIONAL.
PAGE 812. 305 PAGES

...minimize the overall execution time in the presence of interprocessor communication delays.

In most simulation problems, all **program** segments are known in advance. It is, therefore, expedient to employ a static scheduling scheme. This entails extensive preprocessing to determine an optimum scheduling strategy and to allocate all **program** modules to the individual **processing elements** prior to **execution**. Based upon an extensive investigation of previously proposed scheduling algorithms, a suitable algorithmic approach has been developed in the present research. This algorithm **analyzes** the **data flow** graph and **determines** the most appropriate sequence of the **execution** of the nodes comprising the graph. A heuristic is employed to allocate the balanced graph among available **processors** so as to minimize communication **delays**.

The software package permits the user to specify the characteristics of the multiprocessor network and to determine...

27/3,K/41 (Item 6 from file: 35)
DIALOG(R)File 35:Dissertation Abs Online
(c) 2004 ProQuest Info&Learning. All rts. reserv.

865004 ORDER NO: NOT AVAILABLE FROM UNIVERSITY MICROFILMS INT'L.

TEST DESIGN FOR COMPUTER NETWORK PROTOCOLS

Author: SARIKAYA, BEHCET

Degree: PH.D.

Year: 1984

Corporate Source/Institution: MCGILL UNIVERSITY (CANADA) (0781)

Source: VOLUME 45/09-B OF DISSERTATION ABSTRACTS INTERNATIONAL.
PAGE 2990.

...state machines (FSM) to generate sequences for protocols specified as incomplete finite state machines.

For real protocols, **tests** can be designed based on the formal specification of the protocol which uses an extended FSM model...

...a control and a data flow graph. Furthermore, the data flow graph is partitioned to obtain disjoint **blocks** representing the different functions of the protocol. **Tests** are designed by considering parameter variations of the input primitives of each **data flow** function and **determining** the expected outputs. This methodology gives complete **test** coverage of all data flow functions and **tests** for unspecified cases can be designed using the control and data flow graphs. The methodology is applied...

27/3,K/42 (Item 1 from file: 94)
DIALOG(R)File 94:JICST-EPlus
(c)2004 Japan Science and Tech Corp(JST). All rts. reserv.

02558680 JICST ACCESSION NUMBER: 95A0814033 FILE SEGMENT: JICST-E
Availability of HPF Array Elements on Distributed Memories.

HARADA KEN'ICHI (1); LI X (1)
(1) Keio Univ., Fac. of Sci. and Technol.
Joho Shori Gakkai Kenkyu Hokoku, 1995, VOL.95,NO.81(HPC-57), PAGE.91-96,
FIG.2, TBL.2, REF.14
JOURNAL NUMBER: Z0031BAO ISSN NO: 0919-6072
UNIVERSAL DECIMAL CLASSIFICATION: 681.32 681.3.068
LANGUAGE: Japanese COUNTRY OF PUBLICATION: Japan
DOCUMENT TYPE: Journal
ARTICLE TYPE: Original paper
MEDIA TYPE: Printed Publication

...ABSTRACT: distributed memory multiprocessors. In typical numerical applications, a significant portion of data transmissions arises from accesses to **blocks** of array **elements** or regions. Data transmission overheads due to such traffic can be reduced by using compile-time information to detect when data can be reused. This paper presents a **data flow analysis** framework for **determining** the availability of data on a virtual **processor** grid. The data availability information obtained is useful for determining whether an array section can be reused
...

27/3,K/43 (Item 2 from file: 94)
DIALOG(R)File 94:JICST-EPlus
(c)2004 Japan Science and Tech Corp(JST). All rts. reserv.

02339991 JICST ACCESSION NUMBER: 95A0538420 FILE SEGMENT: JICST-E
An Optimization for Data Transmissions on Distributed Memories by Data Flow Analysis.

LI X (1); HARADA K (1)
(1) Keio Univ.
Heiretsu Shori Shinpojiumu Ronbunshu, 1995, VOL.1995, PAGE.43-50, FIG.5,
TBL.3, REF.16
JOURNAL NUMBER: L0460AAV
UNIVERSAL DECIMAL CLASSIFICATION: 681.3.068 681.32
LANGUAGE: English COUNTRY OF PUBLICATION: Japan
DOCUMENT TYPE: Conference Proceeding
ARTICLE TYPE: Original paper
MEDIA TYPE: Printed Publication

...ABSTRACT: distributed memory multiprocessors. In typical numerical applications, a significant portion of data transmissions arises from accesses to **blocks** of array **elements** or regions. Data transmission overheads due to such traffic can be reduced by using compile-time information to detect data which can be reused. This paper presents a **data flow analysis** framework for **determining** the availability of data copy on a virtual **processor** grid. The data availability information obtained is useful for optimizing communication when SPMD **programs** are generated for distributed memory system. A new kind of array section descriptor, called an Section Access Descriptor is proposed, which represents the mapping of an array section onto the **processor** grid. Based on interval data flow **analysis**, data availability at each statement is determined, and redundant communication can be eliminated. (author abst.)

27/3,K/44 (Item 1 from file: 95)
DIALOG(R)File 95:TEME-Technology & Management
(c) 2004 FIZ TECHNIK. All rts. reserv.

00545000 E92033476031
Distributed coputer system: A data flow model
(Verteilte Rechnersysteme: Ein Datenflussmodell)
Alderighi, M; D'Angelo, S; Sechi, GG; Vaccaro, R; Verdoscia, L
CNR Milano, I; CNR Napoli, I
Applied Informatics, Proceedings of the Eighth IASTED International Symposium, Innsbruck, A, February 19-21, 19901990
Document type: Conference paper Language: English

Record type: Abstract
ISBN: 0-88986-142-0

ABSTRACT:

A new **deterministic** model of a **data flow** machine has been presented in this paper. The principal characteristic of this model is that the computation happens in pure dataflow **mode** and no control token is generated during graph execution. This model is well suited for distributed computer...

27/3,K/45 (Item 1 from file: 103)

DIALOG(R)File 103:Energy SciTec

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03922748 EDB-96-006508

Title: Model operating permits for natural gas processing plants

Author(s): Arend, C. (Hydro-Search, Inc., Houston, TX (United States))

Title: Petro-safe '95: 6. Annual environmental, safety and health conference and exhibition for the oil, gas and petrochemical industries. Book 1

Conference Title: Petro-Safe '95 conference and exhibition

Conference Location: Houston, TX (United States) Conference Date: 31 Jan - 2 Feb 1995

Publisher: Houston, TX (United States) PennWell Publishing Company

Publication Date: 1995

p 23-30 (590 p)

Report Number(s): CONF-950152--

Language: English

...Abstract: their compliance status; Determine a strategic method of presenting the general and specific conditions of their Model **Operating** Permit (MOP); Maintain compliance with air quality regulations. A MOP is prepared to assist permitting agencies and affected facilities in the development of **operating** permits for a specific source category. This paper includes a brief discussion of example permit conditions that may be applicable to various types of Title V sources. A MOP for a generic natural gas **processing** plant is provided as an example. The MOP should include a general description of the production **process** and identify emission sources. The two primary **elements** that comprise a MOP are: Provisions of all existing state and/or local air permits; Identification of...

...MOP included in the Title V application will assist in preparation of the final permit provisions, minimize **delays** in securing a permit, and provide support during the public notification **process**.

27/3,K/46 (Item 2 from file: 103)

DIALOG(R)File 103:Energy SciTec

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03813583 CANM-95-0E2988; EDB-95-057351

Title: Performance and emissions of a dual fuel two stroke bus engine

Author(s)/Editor(s): Nowak, P.P.

Corporate Source: Alberta Univ., Edmonton, AB (Canada). Dept. of Mechanical Engineering

Publisher: Edmonton, AB Univ. of Alberta

Publication Date: Fall 1994

(207 p)

ISBN: 0-315-95089-7

Language: English

...Abstract: cylinder air intake port by individual high-speed solenoid valves installed on the outside of the engine **block**. The conversion kit was developed and optimized by preliminary testing on a dynamometer. The engine was tested both in diesel-only and dual-fuel modes at maximum torque and over the Japanese 13- **mode test**. On

dual-fuel, engine **performance** and emissions were studied for two different sets of gas injection tubes, the first deflected toward the ...

...consumption was higher in dual-fuel modes, mainly due to high natural gas losses during the scavenging **process**. The high compression ratio and high air intake charge temperatures limited the diesel fuel replacement ratio; maximum...

...emissions were reduced in dual-fuel modes, while composite CO, hydrocarbon, and soot emissions were higher. Emissions **performance** was better when the gas injection tubes pointed toward the piston. The conversion system **performed** very well over the entire 6V-71 **operating** range and did not cause any technical problems during testing. To optimize this engine for dual-fuel...

27/3,K/47 (Item 3 from file: 103)
DIALOG(R)File 103:Energy SciTec
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03601919 EDB-94-017885

Title: Travel to Europe to collaborate on wind turbine aerodynamics and testing

Author(s)/Editor(s): Simms, D.A.

Corporate Source: National Renewable Energy Lab., Golden, CO (United States)

Sponsoring Organization: DOE; USDOE, Washington, DC (United States)

Publication Date: 15 Oct 1993

(7 p)

Report Number(s): DOE/FTR-94002465

Order Number: DE94002465

Contract Number (DOE): AC02-83CH10093

Language: English

...Abstract: Denmark. Topics discussed at this meeting of the newly-formed Annex centered around planning and coordinating research **objectives** and activities between all of the participants. There are 5 participating organizations from 4 countries, and each will be conducting detailed aerodynamic test **programs** to investigate steady and unsteady aerodynamic behavior on full-scaled wind turbines. The participants gave presentations describing their **test** plans and the **status** of their facilities. Detailed discussions were then held to finalize the formats of the measurement **programs** and information exchange. Initial sample data sets were prepared and exchanged by those participants who already had...

...meeting and site visit was conducted at the ECN facility near Petten, Netherlands. ECN is in the **process** of checking out their aerodynamic pressure force measurement system on a non-rotating outdoor test facility. The 12.5 m blade has been instrumented and installed on a test stand which **holds** the blade horizontally for work access, and tilts it vertically if sufficient winds are available to generate aerodynamics forces to test the measurement system. ECN's test plan has been **delayed** due to rain and low winds all summer. Once these tests are completed and they are sure the system is **operating** as required, they plan to install the blade on their 25 m custom test-bed turbine. On...

...Imperial College Test Facility in the UK. They have completed wind tunnel testing and are in the **process** of building a ruggedized pressure measurement system and refurbishing the Wind Harvester test turbine on which the...

27/3,K/48 (Item 4 from file: 103)
DIALOG(R)File 103:Energy SciTec
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03498064 NEDO-93-960039; EDB-93-076939

Title: Diagnostic apparatus of a fuel cell power generation system

Original Title: Nenryo denchi hatsuden system no shindan sochi

Author(s)/Editor(s): Nagata, Y.

Patent No.: JP 4-342963

Patent Assignee(s): Toshiba Corp., Tokyo (Japan). Principal Office

Patent Date Filed: 21 May 1991

Publication Date: 30 Nov 1992

(7 p)

Language: Japanese

...Abstract: in a previous fuel cell power generation system, which is diagnosed on the obtained data and the **status** of the plant such as an amount of the produced electric power. Therefore, the previous power generation...

...second function generator for the output of a signal of the maximum permissible opening of the flow **detection** valve. Consequently, the **flow path** resistance of the fuel cell power generation system is normal, the direction of the opening of the...

27/3,K/49 (Item 5 from file: 103)

DIALOG(R)File 103:Energy SciTec

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03408012 CANM-92-0E4439; EDB-92-170769

Title: Implementing the databoard concept for SEDA cooperating expert systems

Author(s): Mychaltchouk, P. (Cognisys, Montreal, PQ (Canada)); Marin, M.A. (Inst. de recherche d'Hydro-Quebec, Varennes, PQ (Canada))

Title: Canadian conference on electrical and computer engineering

Author(s)/Editor(s): Bhargava, V.K. (ed.)

Corporate Source: Canadian Society for Electrical and Computer Engineering, Montreal, PQ (Canada)

Conference Title: Canadian conference on electrical and computer engineering

Conference Location: Montreal (Canada) Conference Date: 17-20 Sep 1989

ISSN: 0840-7789

Publication Date: 1989

p 1054-1058 (1183 p)

Report Number(s): CSECE-CE04268 CONF-8909490--; CE--04268

ISBN: 0-9694170-0-4

Language: In English

Abstract: To ensure greater availability and lifespan of its electrical apparatus, Hydro-Quebec follows a diagnosis **process** which indicates the status of the apparatus in question and any maintenance or troubleshooting activities to be undertaken. The **process** may be viewed as a cognitive cycle involving the following steps: work requisition; knowledge of the status of the apparatus in question; validation of the **status** by physical inspection; **tests** to confirm deterioration and/or previous diagnosis; working plan of activities to be **performed**; **execution** of the plan; and updating of the maintenance **program**. Each step of the cognitive cycle can be implemented on a rule-based expert system producing a...

...implementation of analyses into an expert system prototype called SEDA-TRANSFO is described. The system comprises six **operating** modules: operation, inspection, dissolved-gas analysis, tests, repair, and analyses. Each of the five first modules issues...

...four possible verdicts: the apparatus should be disconnected for maintenance; the apparatus should be connected, but with **restrictions**; the apparatus should be connected as-is; and more information is required. 7 refs., 4 figs.

27/3,K/50 (Item 6 from file: 103)
DIALOG(R)File 103:Energy SciTec
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03322901 CANM-92-002997; EDB-92-077199

Title: Niagara 25 Hz system islanded operation tests

Author(s)/Editor(s): Berube, G.R.

Corporate Source: Ontario Hydro, Toronto, ON (Canada). Research Div.

Publication Date: 16 May 1991

(32 p)

Report Number(s): OH/RD-91-119-K MICROLOG--92-02000

Language: In English

...Abstract: of-step protection and leaving the 25 Hz system an electrical island. It was subsequently decided to **perform** needed maintenance work while the system was deliberately islanded, and a test was requested to determine the system stability while in islanded **mode**. A detailed **test** plan was developed and the system was operated as an electrical island for 24 hours, while numerous...

...gate position, terminal voltage, etc.) were monitored to provide detailed data for use in future planning and **operating** studies. The primary **objectives** of the islanded operation tests were to ensure that the isolated operation governor settings provided the necessary...
...the existing system could be operated in the islanded configuration for extended periods of time without any **restrictions** on the operation of customer equipment. The results also confirmed that digital simulations of the system's islanded operation are accurate and can be reliably employed to develop any future planning and **operating** strategies. 4 refs., 18 figs., 3 tabs.

27/3,K/51 (Item 7 from file: 103)
DIALOG(R)File 103:Energy SciTec
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02954465 EDB-90-171709; INS-90-035946

Title: Technical evaluation process for specifying replacement items

Author(s): Craig, W.E. (Cygnus, Atlanta, GA (USA)); Mulford, T.J. (Electric Power Research Institute, Palo Alto, CA (USA))

Conference Title: Winter meeting of the American Nuclear Society (ANS) and nuclear power and technology exhibit

Conference Location: San Francisco, CA (USA) Conference Date: 26-30 Nov 1989

Source: Transactions of the American Nuclear Society (USA) v 60. Coden: TANSA ISSN: 0003-018X

Publication Date: 1989

p 463

Report Number(s): CONF-891103--

Language: In English

...Abstract: the engineering involvement required to specify and procure replacement parts and components continues to increase. The evaluation **process** for a replacement item has one simple goal: to specify an effective set of technical and quality requirements that will result in the procurement of an item meeting the plant design basis. The evaluation **process** is depicted in a simplified **block** diagram showing the **elements** of engineering evaluation from a functional perspective. These **elements** are the following: (1) need for a technical evaluation; (2) components and part classification; (3) failure **mode** and effect **analysis** (FMEA); (4) critical characteristics for design determination; (5) like-for-like or alternate item evaluation; and (6...

27/3,K/52 (Item 8 from file: 103)
DIALOG(R)File 103:Energy SciTec

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02361674 CANM-89-001898; EDB-89-107646

Title: Optimization of primary exploitation strategies for heavy oil reservoirs

Author(s): Miranda, J.; Ashford, F.E.

Affiliation: PDVSA, Venezuela

Title: UNITAR/UNDP, 4th international conference on heavy crude and tar sands, vol. 2

Corporate Source: Alberta Oil Sands Technology and Research Authority, Edmonton, AB (Canada)

Conference Title: 4. UNITAR/UNDP international conference on heavy crude and tar sands

Conference Location: Edmonton, Canada Conference Date: 7-12 Aug 1988

Publication Date: 1988

p 17

Report Number(s): AOSTRA-8808; CONF-880807-; CE-02658

Language: English

Abstract: In order to simulate the **performance** of typical heavy oil reservoirs located in the Orinoco Heavy Oil Field in Venezuela, two reservoir models...

...constructed: one to simulate the specific behavior of the MFB-109/U2 sand reservoir of the Bare **block**, and a second to simulate a more general type of reservoir of the Hamaca area. Confidence in the predictive models was obtained by a satisfactory history match of the production **performance** of the MFB-109/U2 sand reservoir. Modelling of a water/oil transition zone was required to...

...bubbles, to obtain an acceptable match of the gas-oil-ratio history. The models, in the prediction **mode**, were used to **evaluate** the sensitivity of heavy oil reservoir **performance** at various **operating**, development, and completion strategies as well as for well spacing, and aquifer size analyses. The sensitivity study...

27/3,K/53 (Item 9 from file: 103)

DIALOG(R)File 103:Energy SciTec

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00531628 AIX-10-433556; EDB-79-111093

Title: General behaviour of flow induced vibrations in helical tube bundle heat exchangers

Author(s): Chen, Y.N. (Sulzer Bros. Ltd., Winterthur (Switzerland))

Source: Sulzer Tech. Rev. (Switzerland) v Spec. No. Nuclex 78. Coden: SZTRA

Publication Date: 1978

p 59-68

Language: English

...Abstract: broad band turbulence rather than vortex shedding. The critical tube vibration was found to be the third **mode**, and the corresponding lift coefficient to be $C_{sub}(L) = 0.083$ for an average amplification factor of...

...of the excitation. A fluid-elastic instability arising from coupling between the elastic tube vibration and the **flow path** variation was **detected** at the upper limit of the velocity range tested. This instability seems to be restricted to the...

27/3,K/54 (Item 10 from file: 103)

DIALOG(R)File 103:Energy SciTec

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00415526 PRD-78-011194; EDB-78-114707

Title: Three Mile Island Nuclear Station, Unit 2. Reportable occurrence No. 78-023/03L-0: LER re determination, while in Mode 4 on 25 March

1978, of only one boron source flow path verified for operation,
due to a clerical error (Only one boron source flow path verified for
operation, due to a clerical error)

Corporate Source: Metropolitan Edison Co., Reading, PA (USA)

Publication Date: 24 Apr 1978

p v

Report Number(s): DOCKET-50320-531

Language: English

...Title: Island Nuclear Station, Unit 2. Reportable occurrence No.
78-023/03L-0: LER re determination, while in Mode 4 on 25 March
1978, of only one boron source flow path verified for operation,
due to a clerical error

27/3,K/55 (Item 1 from file: 144)

DIALOG(R)File 144:Pascal

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12986322 PASCAL No.: 97-0265287

Glass furnace NO SUB x control with gas reburn : The field test

Collection of papers presented at the 57th conference on glass problems :

Columbus OH, October 8-9, 1996

KOPPANG R; MARQUEZ A; MOYEDA D; JOSHI M; MOHR P; MADRAZO R

DRUMMOND Charles H, ed

Energy and Environmental Research Corporation, Irvine, California ,
United States; Combustion Tec Inc., Apopka, Florida, United States; Anchor
Glass Container Corporation, Tampa, Florida , United States

Conference on glass problems, 57 (Columbus OH USA) 1996-10-08

Journal: Ceramic engineering and science proceedings, 1996, 18 (1)
120-135

Language: English

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Glass furnaces are significant emitters of NO SUB x due to their high
operating temperatures. The glass industry is faced with progressively
more stringent environmental regulations, particularly for NO SUB x , in
regions of ozone nonattainment. The overall **objective** of this development
and demonstration **program** is to commercialize gas reburn technology (GR),
previously successfully applied in large boilers and incinerators, to large
glass furnaces commonly found in the manufacture of flat, container, and
specialty glass. Previous phases of the **program** have been presented in
GRI Topical Reports and at the 1995 Conference on Glass Problems. The
technical...

...to a large, sideport regenerative furnace manufacturing container glass.
Hardware concepts to implement each of three primary **process** steps is
described: primary combustion with reduced excess O SUB 2 operation, port
neck gas reburn zone...

... demonstration site at Anchor Glass Container's Antioch plant was judged
to be compatible with the GR **process** and one furnace has been modified to
implement full gas reburning with the **objective** of demonstrating up to
70% control in the GR **operating mode** . The **test** and **evaluation** phase
of the demonstration is being **performed** in three discrete test **blocks** :
furnace optimization for as-found and gas reburn operation, GR startup
through serial commissioning of port pairs, and parametric **performance**
tests at progressively higher gas reburn flows followed by long-term
operation at an optimized condition. The...

27/3,K/56 (Item 1 from file: 202)

DIALOG(R)File 202:Info. Sci. & Tech. Abs.

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1100563

Performance analysis of a data-flow processor.

Book Title: Report Mac-csg-m-128. Contract N00014-75-c-0661. 1975 August.
Project Mac, Massachusetts Institute Of Technology, Cambridge. 8 P. Ntis:
Ad-a014 567; Hc \$3.25, Mf \$2.25. See Isa 76-603/s.
Author(s): Misunas, David P
Publication Date: 1975
Language: English
Document Type: Book Chapter
Journal Announcement: 1100

A data-flow **processor** is structured as a **packet** communication system. Sections of a **processor** are connected by interconnection networks which have a great deal of inherent parallelism, and the sections communicate by means of fixed size information pockets. The **processing** capability of a **data - flow processor** is **determined** through consideration of the flow of **packets** within the interconnection networks, and the actual **performance** of the **processor** is affected by the structure of the networks. The **execution** time of an instruction in a **processor** can vary greatly due to conflict within the interconnection networks. The **performance** of a data-flow **processor** is measured through consideration of the **delays** caused by this conflict, and the proper network structure and **processing** rate of a machine are determined through **analysis** of the best and worst case **delays**.

27/3,K/57 (Item 1 from file: 233)
DIALOG(R)File 233:Internet & Personal Comp. Abs.
(c) 2003 EBSCO Pub. All rts. reserv.

00487229 98IW02-311
MonitorMQ 2.5 deftly tracks MQSeries messages
Biggs, Maggie
InfoWorld , February 23, 1998 , v20 n8 p108, 1 Page(s)
ISSN: 0199-6649
Company Name: Level 8 Systems
Product Name: MonitorMQ 2.5

...HTML making it possible to manipulate with the use of a browser. Says it displays realtime information; **tracks** the **status** of message queues, events, communications, and message flow; and helps IT managers to browse message queues and **identify** potential **data flow** problems in MQSeries environment. However, says its Web-server support is limited to Windows NT, and certified...

27/3,K/58 (Item 1 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
(c) 2004 The Gale Group. All rts. reserv.

02357713 SUPPLIER NUMBER: 58237255 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Web demands highly available ports. (Zynx Corp) (Company Business and Marketing)
Hayes, Tim
Electronic Engineering Times, 116
Dec 13, 1999
ISSN: 0192-1541 LANGUAGE: English RECORD TYPE: Fulltext
WORD COUNT: 1089 LINE COUNT: 00093

... which can make RAL features available on next-generation MACs.
As the layer that interfaces with the **operating** system, the PAL layer contains instrumentation for management. At the application layer, the PDK provides a Posix...

...number of critical devices in the system. Areas and management information bases monitored include: MAC MIB for **packets**, errors, **interrupts**, etc.; Speedometer MIB for **packets** per second, percent utilization and high watermarks, and Trunk MIB for active and inactive members and operational **status**. The Speedometer **tracks** a collection of statistics that are relevant to **performance**.

27/3,K/59 (Item 2 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)

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02273424 SUPPLIER NUMBER: 53969934 (USE FORMAT 7 OR 9 FOR FULL TEXT)

PC Management: Intel And IBM Announce Next Generation PC Alerting With Alert On LAN 2 Technology; Advanced Manageability Alliance Extends Its Leadership Role in PC Management and Protection.(Company Business and Marketing)

EDGE: Work-Group Computing Report, NA

March 1, 1999

LANGUAGE: English RECORD TYPE: Fulltext

WORD COUNT: 848 LINE COUNT: 00073

TEXT:.

...problem, but now has the ability to take corrective action by remotely rebooting the PC into a **diagnostic mode** and fixing the problem. Integration of Alert on LAN 2 capabilities with existing systems management applications help...

...management toolkit. Maintenance capabilities have also been expanded with Alert on LAN 2 helping network administrators to **perform** common system support functions, such as remote **operating** system (OS) repairs or BIOS image updates, in response to system failures. In addition, a higher level...

...99 specifications. The success of the AMA is due to joint development of technology, delivering industry building **blocks**, driving broad deployment and extending industry specifications to enable widespread industry adoption. "IBM's Advanced Manageability Alliance...

27/3,K/60 (Item 3 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)

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02263331 SUPPLIER NUMBER: 53633862 (USE FORMAT 7 OR 9 FOR FULL TEXT)

Y2K: Survey Finds Firms Gearing Up on Contingency Planning.(Industry Trend or Event)

EDGE: Work-Group Computing Report, NA

Jan 25, 1999

LANGUAGE: English RECORD TYPE: Fulltext

WORD COUNT: 933 LINE COUNT: 00081

TEXT:

...the more encouraging results, contingency planning for the Year 2000 appears to have the attention of business **executives**, not just computer professionals. Seventy-eight percent say this work stretches across the enterprise. Forty-three percent...

...involvement and support of the CEO. While contingency planning appears to have the buy-in of top **executives**, the actual depth and substance of this work raises questions. Survey results suggest that in many firms...

...is just one more job for their IT shops. Fifty-seven percent of respondents say their organizations **perform** contingency planning from the Y2K **program** office, primarily using IT managers and professionals. This even though 71 percent of respondents define contingency planning...

...percent cite supply chain management. Only 18 percent of respondents say their organizations have appointed a key **executive** to **perform** this role on a full time basis; 39 percent have no such **executive** in place. Supply chain management is often seen as a key consideration for assuring business continuity. Again, however, the survey seems to generate questions about the how aggressively companies are pursuing the contingency planning **process**. Although 44 percent said they would **stop** doing business with

companies found to be non-compliant, only 33 percent said they would actually visit...

...this notion. Over one-third said the bug has already started to bite, triggering failures under actual **operating** conditions. Of those reporting specific failures, these included data exchange errors (34 percent), accounting errors (27 percent)...

...software causing rework (25 percent), data base file corruption (21 percent), and computer crashes (18 percent). In **test mode**, 71 percent of respondents are finding failures. "The good news is that 'actual errors' rates appear to..."

...group on the Year 2000 software conversion issue. The ITAA Year 2000 Task Force conducts a full **program** of Year 2000-related activities, all aimed at helping customers in government and the private sector respond...

...and effectively to the software conversion challenge. A separate white paper, vendor directory, buyer's guide, certification **program**, alternative dispute resolution **program**, vendor/customer questionnaires, and other materials on the Year 2000 issue are available on the ITAA web...

27/3,K/61 (Item 4 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
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01977200 SUPPLIER NUMBER: 18615941 (USE FORMAT 7 OR 9 FOR FULL TEXT)
A model for platform development. (HP's software development strategy)
(Company Operations)
Jandourek, Emil
Hewlett-Packard Journal, v47, n4, p56(16)
August, 1996
ISSN: 0018-1153 LANGUAGE: English RECORD TYPE: Fulltext; Abstract
WORD COUNT: 12086 LINE COUNT: 01024

... in a given piece of information increases. Putting together a communication model in the form of a **data flow** diagram helps teams **identify** who needs to know about plans, assignments, issues, **status**, best practices, and successes. However, getting information to flow is not enough because the recipients of the...

27/3,K/62 (Item 5 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
(c) 2004 The Gale Group. All rts. reserv.

01822641 SUPPLIER NUMBER: 17172619 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Inside the P6. (aggressive design of new Intel P6 microprocessor) (includes related article on how P6 bus is optimized for speed and multiprocessing)
Stam, Nick
PC Magazine, v14, n15, p118(8)
Sep 12, 1995
ISSN: 0888-8507 LANGUAGE: English RECORD TYPE: Fulltext; Abstract
WORD COUNT: 5499 LINE COUNT: 00439

... is where the retirement stages of the pipeline, discussed below, come into the picture.

The P6 performs **data - flow** analysis to **determine** which micro-ops have all of their operands ready and can be dispatched to the execution units...

...that can hold a P6 micro-op with two associated operands, a result, and a number of **status** bits. Both floating-point and integer micro-ops can reside in the ROB.

RETIREMENT
Instructions that are...

27/3,K/63 (Item 6 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
(c) 2004 The Gale Group. All rts. reserv.

01615054 SUPPLIER NUMBER: 14149680 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Boundary routing for network management. (Special Internetworking Section)
Joyce, Richard
Telecommunications, v27, n7, p62(3)
July, 1993
ISSN: 0278-4831 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT; ABSTRACT
WORD COUNT: 1663 LINE COUNT: 00131

... is introduced, access routers need to be changed. Boundary routers need no modification.

A detailed analysis of **data flow** and internetwork requirements **identified** one area that offered potential for a significant simplification of the routers -- sites on the periphery of...

...remote sales offices linked by a WAN network. Typically, the network might be used to check order **status** interactively. In this setup the router in the core mesh, such as one at the Dallas site...

27/3,K/64 (Item 7 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
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01446931 SUPPLIER NUMBER: 11048202 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Know your FPUs. (detecting different floating point units) (technical) (tutorial)
Stimpson, Bob
EXE, v6, n1, p54(5)
June, 1991
DOCUMENT TYPE: tutorial ISSN: 0268-6872 LANGUAGE: ENGLISH
RECORD TYPE: FULLTEXT; ABSTRACT
WORD COUNT: 2524 LINE COUNT: 00189

TEXT:

...Information Technologies (IIT) have joined the pack. All provide PC Floating Point Units (FPUs) or maths co-**processors**. FPUs fall into two families - the peripheral and the memory-mapped types. This article concentrates on peripheral...

...for memory-mapped FPUs. Differences between the intel compatibles are subtle, but can prove useful from the **programming** point of view as well as from the obvious benefit of enhanced number crunching speed. These are outlined and the detection routines explained, along with a brief **performance** comparison between the Intel, Cyrix and IIT offerings. standards The standard to which all current FPUs (and most current compilers) store and **process** real numbers is the IEEE 754 (1985) Standard for Binary Floating Point Arithmetic specification. Figure 1 shows...

...separate chip. The intel standard provides users with eight 80-bit registers and a comprehensive set of **operating** instructions acting in stack type operations. All data is converted upon loading to an 80-bit Temporary...

...to access the Weitek segment. (The Microsoft EMS386.SYS driver has a switch to do just this.) **Processor** read instructions from this memory are interpreted as data transfer commands, and **processor** write instructions as operation commands. This has the benefit over the Intel convention in being able to...detection routines are included here for completeness. The 8087 is the only FPU to respond to the **interrupt** bit in the FPU control word. By loading the control word with the bit cleared and then...

...the 486. This is privilege level sensitive Virtual 86 mode, so it cannot be universally applied without **restrictions**. Surprisingly, Intel demonstrates this method in the latest 487SX specification - but there is a

more rigorous and universally applicable technique which is independent of the need to know **processor** type - and uses Intel-published information (Intel 486SX Microprocessor/487SX Math Coprocessor Specification, April 1991, Pan Number...

...with the correct byte to verify the FSAVE bits are zero. 386 Mode The 16/32-bit **mode test** checks if the **Status** Word is the second in the FSAVE structure by looking for an 18h signature in the...

...is simply around half the price of the comparable Intel chip. AMD literature does not provide any **performance** comparisons, but if price is the only guide, it is possibly one of the best price-**performance** boosts a 286 PC could receive. Cyrix manufactures i387 and 387SX clones (the CX-83D87 and CX-83S87) which **process** values with better accuracy (one or two LSBs closer to the true result in extremes) and lower...

...Although uniquely identifiable as Cyrix using software, the benefits are reaped by the user rather than the **program** developer. This will not affect code design, but could affect your recommendations for preferred hardware and presenting...

...4 to detect the Cyrix FPU. if the $y = 1[\sup.x] - 1$ or F2XM1 instruction is **executed**, with x outside the range -1.0 [is less than or equal to] x [is less than...has extended 387 functionality further by using a fourth set of registers (which are inaccessible to the **programmer**) to permit vector multiplication by a 4 x 4 matrix (the size commonly required for 3D graphics operations) using only one FPU command, once the registers are loaded with matrix **element** values. The ability to **process** this multiplication-intensive problem with a single instruction in hardware reduces the calculation time by a factor of four or so when compared with a conventional 80387. Conclusions To evaluate these FPUs with reasonable **objectivity**, and to provide a rudimentary guide to relative **performance**, I devised a test **program** which consisted of two hundred 287 instructions covering most of the instruction set in realistic problems (see...

...run on AST, Compaq and Viglen hardware, using Intel, Cyrix and IIT FPUs. The IIT 2C87 returned **performance** times consistently 75% faster than the Intel 287. The test run on a Compaq 386/20E with an Intel 80387 and an AST 386-20 with a Cyrix 83D87 indicated a 30% improvement in **performance** between Cyrix and Intel - but this is not a perfect comparison. if you are contemplating using the FPU in your **program** design, the choice is wider than ever before. I hope this has provided some insight into a little understood but widely available hardware upgrade which can transform software **performance**. My own personal conclusion is that best value is the cheapest FPU on offer - which is also...

...a Systems House based in the North West. His personal interest lies in techniques which boost PC **performance** and provide useful information on PC configuration. Many thanks to Alan Milosevic of DMST (0635 247100)for...

27/3,K/65 (Item 8 from file: 275)

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01358605 SUPPLIER NUMBER: 08474952 (USE FORMAT 7 OR 9 FOR FULL TEXT)

Toolset breaks new ground in RISC software development. (The Portland

Group's SuperCompiler supports automatic software pipelining and

instruction scheduling for reduced-instruction-set computer processors)

Williams, Tom

Computer Design, v29, n9, p18(2)

May 1, 1990

ISSN: 0010-4566

LANGUAGE: ENGLISH

RECORD TYPE: FULLTEXT; ABSTRACT

WORD COUNT: 1155

LINE COUNT: 00093

... track the status of variables that have been stored in registers, for instance; and using registers to **hold** variables is a common optimization technique. If a new routine puts its variables in a register set previously occupied by the variables of a routine that has finished

executing , the debugger might not know to look for the first routine's variables back in memory. Denison...

27/3,K/66 (Item 9 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
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01305542 SUPPLIER NUMBER: 07742771 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Utilities and languages. (listings of software programs) (directory)
DG Review, v7, n1, p4(7)
Summer, 1989
DOCUMENT TYPE: directory ISSN: 1050-9127 LANGUAGE: ENGLISH
RECORD TYPE: FULLTEXT; ABSTRACT
WORD COUNT: 8309 LINE COUNT: 00734

... emulator. Price: \$3,300 one-time installation plus \$8,000/year license and maintenance CPU: MV/Family **Operating** Systems: AOS, AOS/WS, AOS/VS, AOS/DVS Reference Number: 996 Contact: Daniel A. Ausderau Telephone: +4121...

...to an MV/Family system running the Tape Management System (Master). Price: Contact vendor CPU: MV/Family **Operating** Systems: AOS/VS Reference Number: 774 Contact: Paula Jacobs Telephone: (508) 898-4183 DATA GENERAL CORP. SOFTWARE...

...Data General can also develop customized enhancements for specific customer requirements. Price: Contact vendor CPU: MV/Family **Operating** Systems: AOS/VS Reference Number: 887 Contact: Paula Jacobs Telephone: (508) 898-4183 DATALYNX, INC. Application: Profile...

...to be reused and can generate new passwords that are easy to remember. User access can be **restricted** by time or membership in a group. Using one screen, the system manager can control 47 profile...

...set of reports is available, detailing profiles by a variety of criteria. Price: \$700 CPU: MV/Family **Operating** Systems: AOS/VS Reference Number: 975 Contact: John Husing Telephone: (619) 560-8112 DMS SYSTEMS, Inc. Application...

...Optimizer and Management Utility (DiskOpt) Description: DiskOpt reorganizes disks to minimize disk-head movement for improved system **performance** . Directories are placed together, files are made contiguous and free space is consolidated. Frequently accessed files can...

...is fully menu-driven for ease of use and interactive to provide feedback to users. Features include **analysis mode** , fast PCOPY-type dump and load, batch options, ability to handle bad **blocks** and disk usage statistics. Price: Contact vendor CPU: MV/Family **Operating** Systems: AOS/VS Reference Number: 929 Contact: Eric R. Fredrickson Telephone: (801) 484-3333 GERRY MANNING & ASSOCIATES...

...16- and 32-bit COBOL versions are also available. Price: \$499 CPU: Desktop, MicroEclipse, Eclipse, MV/Family **Operating** Systems: AOS, AOS/VS Languages: COBOL, Interactive COBOL Reference Number: 871 Contact: Gerry Manning Telephone: (512) 343-9106 GERRY MANNING & ASSOCIATES Application: Multiterminal ICOBOL for PCs Product Name: ICHOST Description: ICHOST has high- **performance** , expandable Interactive COBOL capability in MS-DOS, UNIX and XENIX environments on IBM PC/XT/AT or...

...solution available for low terminal counts. Innovative add-in boards with additional CPUs and memory give maximum **performance** at high terminal counts. Price: Contact vendor CPU: Dasher/286, IBM PC/XT/AT and compatibles **Operating** Systems: MS-DOS, UNIX, XENIX Languages: Interactive COBOL Reference Number: 872 Contact: Gerry Manning Telephone: (512) 343...

...Description: Threshold's ICOBOL to RM/COBOL Translator provides fast, accurate syntax translation of Data General ICOBOL **programs** can then be run on any CPU with RM/COBOL. Complex screen section options are supported.

Faithful emulation of the ICObOL environment is given to call **program** , switches on **program** names and function keys. Many options and parameters are user-defined, including COPY FILE name translation, which...

...and user diagnostics and warnings are inserted when appropriate. Price: Contact vendor CPU: DG/One, MV/Family **Operating** Systems: AOS/VS, MS-DOS Languages: C Reference Number: 843 Contact: Bob Head Telephone: (205) 821-0075 capabilities for **programs** written in ICObOL or 32-bit COBOL on AOS/VS systems. Features include faster display speed, redraw...

...mail, call CLI and more. All of these functions are available without modifying or even recompiling existing **programs** . Other routines are provided to allow COBOL **programs** to use overlapping windows with minimal **programming** . Other **programs** may be attached to function keys so they may be called from anywhere in the application. Price: \$750; demo: \$25 CPU: MV/Family **Operating** Systems: AOS/VS Language: COBOL, ICObOL Reference Number: 945 Contact: Bob Head Telephone: (205) 821-0075 UTILITY COMPUTING SERVICES Application: Source code conversion Product Name: **Program** Converters Description: **Program** Converters convert **program** source code to native source code for other systems; to DG COBOL from RM, TI and Wang ...

...DG/One, Dasher/One, Dasher/286, Desktop, MicroNova, Nova, MicroEclipse, Eclipse, MV/Family, Nova compatible, Eclipse compatible **Operating** Systems: AOS/VS Languages: COBOL, Interactive COBOL Reference Number: 956 Contact: Richard R. Leinbach Telephone: (315) 736...
...Extended BASIC. Price: \$2,500 CPU: Desktop, MicroNova, Nova, MicroEclipse, Eclipse, MV/Family, Nova compatible, Eclipse compatible **Operating** Systems: DOS, RDOS, AOS, AOS/WS, AOS/VS Languages: Extended BASIC Reference Number: 952 Contact: Daniel Harvey...

27/3,K/67 (Item 10 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
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01290052 SUPPLIER NUMBER: 07102870 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Abstracts from other ACM publications.
Communications of the ACM, v32, n3, p382(5)
March, 1989
ISSN: 0001-0782 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT
WORD COUNT: 5564 LINE COUNT: 00476

... method for global optimization and code motion by suppression of partial redundancies [1]. Morel and Renvoise use **data flow analysis** to **determine** expression computations that should be inserted at the end of certain basic **blocks** and to determine redundant computations that can be eliminated. The **execution** of these techniques results in the movement of loop invariant expressions out of the loop. In addition...

27/3,K/68 (Item 11 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
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01261998 SUPPLIER NUMBER: 07197865 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Utilities and languages. (buyers guide)
DG Review, v6, n2, p4(7)
Winter, 1988
DOCUMENT TYPE: buyers guide ISSN: 1050-9127 LANGUAGE: ENGLISH
RECORD TYPE: FULLTEXT; ABSTRACT
WORD COUNT: 9439 LINE COUNT: 00836

... Optimizer and Management Utility (DiskOpt) Description: DiskOpt reorganizes disks to minimize disk-head movement for improved system **performance** . Directories are placed together, files are made contiguous and free space is consolidated. Frequently accessed files can...

...is fully menu-driven for ease of use and interactive to provide feedback to users. Features include **analysis mode**, fast PCOPY-type dump and load, batch options, ability to handle bad **blocks** and disk usage statistics. Price: Contract vendor CPU: MV/Family **Operating** systems: AOS/VS Reference Number: 929 Contact: Eric R. Fredrickson Telephone: (801) 484-3333

GERRY MANNING & ASSOCIATES...

27/3,K/69 (Item 12 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
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01244612 SUPPLIER NUMBER: 06689975 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Highly polished C code. (Software Review) (comparison of eight C compilers) (evaluation)
Hisley, Philip N.
PC Tech Journal, v6, n6, p76(15)
June, 1988
DOCUMENT TYPE: evaluation ISSN: 0738-0194 LANGUAGE: ENGLISH
RECORD TYPE: FULLTEXT; ABSTRACT
WORD COUNT: 6662 LINE COUNT: 00539

... the frequency of variable use, the life span of currently enregistered variables (as determined by data flow **analysis**), and the number of available registers. Depending on the degree of optimization **performed** by a compiler, the life span of a variable could be within a single statement, within a basic **block**, or over several basic **blocks**. A variable is kept in a register only if

27/3,K/70 (Item 13 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
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01204651 SUPPLIER NUMBER: 05042731 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Solve cost-speed conflicts with VMEbus controller.
Nester, Steve; Pryor, Kent
Electronic Design, v35, p99(4)
July 9, 1987
ISSN: 0013-4872 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT
WORD COUNT: 2606 LINE COUNT: 00208

... start multiplying exponentially for queue lengths above three, the three command ports represent a sufficient number to **hold** the request queue during normal operation of most typically-loaded systems. A large buffer RAM and four DMA channels are available for these operations (Fig. 3). In operation, an application **program** running on the host CPU issues a request to a storage device. This is typically directed to an **operating** -system service routine that determines the type of request. Control is then transferred to a software driver, which prepares a command, **tests** the **status** of the hardware controller, and directs the controller to **perform** a specific function, such as a disk seek. After monitoring the disk activity, the peripheral will send an **interrupt** to the host CPU when the appropriate data track has been located. The **operating** system then takes control of the data-transfer operation (Fig. 4).

With a single-command-at-a...

27/3,K/71 (Item 1 from file: 647)
DIALOG(R)File 647:CMP Computer Fulltext
(c) 2004 CMP Media, LLC. All rts. reserv.

01206267 CMP ACCESSION NUMBER: EET19991213S0075
Web demands highly available ports
Tim Hayes, Principal Telephony Architect, Advanced Research Lab, Znyx Corp., Santa Barbara, Calif.
ELECTRONIC ENGINEERING TIMES, 1999, n 1091, PG116

PUBLICATION DATE: 991213
JOURNAL CODE: EET LANGUAGE: English
RECORD TYPE: Fulltext
SECTION HEADING: Embedded Systems - Focus: The Reliability Factor
WORD COUNT: 1005

... which can make RAL features available on next-generation MACs.

As the layer that interfaces with the **operating** system, the PAL layer contains instrumentation for management. At the application layer, the PDK provides a Posix...

...number of critical devices in the system. Areas and management information bases monitored include: MAC MIB for **packets**, errors, **interrupts**, etc.; Speedometer MIB for **packets** per second, percent utilization and high watermarks, and Trunk MIB for active and inactive members and operational **status**. The Speedometer **tracks** a collection of statistics that are relevant to **performance**.

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27/3,K/72 (Item 1 from file: 674)
DIALOG(R)File 674:Computer News Fulltext
(c) 2004 IDG Communications. All rts. reserv.

077294

Y2K concerns mounting: International travel troubles loom

Byline: Ephraim Schwartz

Journal: Network World

Publication Date: August 24, 1999

Word Count: 920 Line Count: 86

Text:

... Italy, Russia, Saudi Arabia, and Thailand are some of the candidates for the list. That means companies **operating** in those countries and sending business travelers to them need to begin thinking about developing action plans...

... Kathleen Vokes, a spokeswoman for Ford Motor, in Dearborn, Mich. Although Ford is not placing any travel **restrictions** on its employees, according to Vokes, a recent year-2000 report from the Gartner Group suggested that...

... Transportation, will go online in September with its worldwide assessment of airline safety. The assessment will include **evaluations** of the readiness **status** of air-traffic control systems, global airport infrastructure including security, and the aircraft themselves. The information will...

... operation is right now touring Asia, doing operational reviews around Y2K," said Tom Sadtler, director of marketing **programs** at Compaq Customer Services, in Stow, Mass. One IBM representative said the services division is asking its...

... Stamford, Conn. According to Kyte, the government of China recently issued a directive requiring that the top **executives** of airlines "must be in the air on 1 Jan. 2000."

27/3,K/73 (Item 2 from file: 674)
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076689

Tracking software usage

KeyServer is tops among six software metering programs that tackle similar tasks in different ways.

Byline: JAMES GASKIN, NETWORK WORLD TEST ALLIANCE

Text:

... reasons you should invest in a software metering application: To stay legal and to save money. These **programs** can help you buy enough licenses to cover your legal requirements, but not one copy more. Software metering technology has matured to the point that metering accuracy is almost guaranteed. All the **programs** we looked at track every application users run on their computers, ensure there is a valid license for each **program** and **stop** people from running inappropriate and unlicensed software. What distinguishes applications is how they handle distributed metering stations, enterprise scalability and management. Some **programs** focus on the users, others on the applications. All six **programs** we tested can **block** server and local application usage. All have time-sensitive controls, so you can make games off-limits until after 5 p.m. Several **programs** report to higher-level management suites, such as Microsoft's System Management Server (SMS) and Novell's...

... NDS). Queues for applications are popular features, as are tricks to verify that licenses are held by **functioning**, nonlocked computers. Every metering **program** has some method of allowing VIP users to access applications even when they would otherwise be locked out. With all this in common, you might expect these **programs** to be quite similar. You'd be wrong. KeyServer 5.0 from Sassafras Software is unlike any of the other **programs** we tested, particularly in its small size and light server requirements. When a product is "something different..."

... it hard to consider Lan Licenser an enterprise heavyweight. Small wonder the single installation diskette - yes, diskette - that **holds** KeyServer 5.0 belies the sophistication of the **program** and left us wondering how Sassafras Software crammed so much into such a small package. KeyServer supports...

... may have the limited-support IPX client provided by Microsoft. KeyConfigure, Sassafras Software's configuration and administration **program**, runs on almost any Windows 95, 98 or NT or Macintosh system. The only requirement concerns applications...

... environment variable on each machine. As soon as the client software is installed and configured, all client **programs** on local computers and servers are swept up into the KeyServer database. All that was left for us to do was select one of three levels of control for each **program**: ignore, log or control. The **programs** we instructed KeyServer to ignore, such as system files, were properly ignored from that point on. Logged **programs** are simply tracked and reported. Controlled **programs** have a bite taken from them and stored on KeyServer. Until KeyServer provides the key piece of...

... WRQ's Express Software Manager 4.5 wasn't the greatest. When we tried to install the **program**'s Express Console on a Windows 98 system, the files on the CD-ROM didn't match the file names the installation **program** was looking for. Although the excellent 523-page manual says the Express Console component can be loaded...

... WRQ requires NT to run Enterprise Services, which automates portions of the software library and client installation **processes**. Taking a guess, we moved to the Windows NT system and installation went smoothly. From that point on, we were impressed with the **program**, particularly its extensive selection of reports and well-designed interface. WRQ allowed us to store application library...

... ships. Integration with Microsoft's SMS is available. To begin metering, we had to run a single **program** on each workstation, through a logon script or manually. The DOS **program** quietly scans the local system's hard disk and sends the information to the Express Console quickly...

... Knowledge Base. Applying the Knowledge Base to the autodiscovered list of applications identifies just about every single **program** on each client. You can then detail unrecognized applications and add them to the Knowledge Base. With...

... glitches. The Windows 98 station acting as the administrator console came up with errors when starting the **program**, and we had to reboot it. Elron should reboot by default instead of giving you the option to run the **program** immediately. We also had to manually add the batch file that starts the NetWare server portions of...

... their NDS activity, so use of server-based applications is monitored immediately. Local applications require a local **program** to run and report back to the server. A check box tells SofTrack to alert the network manager when local control files are bypassed, allowing nonmetered access to local applications. The manual says the **program** can't track DOS applications within a Windows DOS box, but SofTrack immediately caught our 3C5x9CFG.EXE network board configuration DOS **program** within a Windows 95 client. You can select applications to monitor from a list of **programs** discovered by the SofTrack servers, or you can type application **executable** files by hand. If your client applications are stored on local and network drives, you must add a client **program** to track local files and monitor them separately. Reports cover a variety of management details and are into Novell's Licensing Service Application **Program** Interface (LSAPI) implementation. Installation was smooth, including configuration of the administration console system. You can manage multiple...

... are ready to read with the morning coffee. Tying the client agent software to the network logon **process** is clean and simple if all clients log on to the network. For stations that can get...

... 95, 98 and NT machines. CentaMeter supports three types of metering: passive metering counts the applications used; **restricted** metering **blocks** access without a license; and LSAPI-enabled metering monitors application use and sends information to the license...

... licensed products, a complete bundle or both. CentaMeter's 31 reports are generated from a separate report **program** that starts automatically when you click the "Extended Report" option. For custom reports, you can build special...

... Funk Software works on NetWare and NT servers, easily integrates NDS users and has a "stealth" metering **mode** that lets you **track** server-based applications without any software on the client. Our biggest gripes? The poor directions in the...

... which required us to work our way through five disks. Fortunately, the server part of the installation **process** was easy. Servers appear automatically, along with their hard-disk volumes. Transport protocol choices include Named Pipes...

... pulls information from NDS, but it doesn't offer a plug-in for Novell's NetWare Administrator **program**. However, its console **program** is a good one. Servers are managed individually from a single console, and most reports are server...

... required referenced files couldn't be located by the client, even though we chose the Network Install **Program** for Clients during installation. But when we tried the stealth mode, which is tied to NDS user...

... umbrella name, such as Spreadsheets for a licensed group, and browse the application lists. The main management **program** reveals a list of available servers; license details, including host server, license name and a description typed...

... permanent, good until a certain date or good for a certain number of days after installation. The **program** lets you assemble suites by grouping individual applications. You can meter local applications by "wrapping" them with...

... talked with technical support. We didn't end up changing anything, just trying again. Running the client **programs** caused Windows Registry problems so severe the Windows 95 machine couldn't work even in Safe mode ...

... representative was well-versed in how to reverse the damage by hacking the Registry. Kick-starting server **processes** required manually **stopping** and restarting them. As indicated by its Back Office certification, Lan Licenser aims at the enterprise and...

... up to NetWare 4.X under bindery emulation; there's no support for NDS. Once installed, the **program** offers some nice touches. A Quick Start page on the console puts all the major functions one...

... new Metering Server. The flexible console includes multiple wizards and help files. Lan Licenser catches every small **program** that goes between systems and provides good control over suites and their internal applications. Three pull-down...

... and Adobe PDF formats. The product's 15 standard reports offer more graphical options than the other **programs**, including 2-D and 3-D

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074752

BoostWeb Enterprise bolsters Web apps

Tests yield two- to threefold performance gain, but customer service, support need work.

Byline: DENNIS WILLIAMS NETWORK WORLD TEST ALLIANCE

Journal: Network World Page Number: 29

Publication Date: May 17, 1999

Word Count: 819 Line Count: 77

Text:

nytime a company claims its software can increase **performance** of any kind by tenfold, we raise our red flag of skepticism. BoostWorks asserts that its BoostWeb Enterprise, which shipped this month, can increase the **performance** of Web-based applications anywhere from four to ten times. While our **tests** indicate more modest gains, we were impressed that the software can raise the **performance** of Web-based applications by two to three times. However, the product is not without flaws in conception and **execution**. Because it uses a client/server architecture, you only see gains from BoostWeb Enterprise on your own...

... intermediary proxy server will see no improvement. And BoostWeb Enterprise fails to provide network administrators with adequate **performance** feedback. You get no indication of how much benefit you're deriving from the product because while...

... capabilities of the company's earlier products, BoostWeb Intranet and BoostSQL, which are designed to bolster the **performance** of Intranet and SQL applications over congested links. BoostWeb Enterprise extends the intranet version by making it...

... a slow link, BoostWeb Enterprise tries to maximize the efficiency of the link. To do this, it **analyzes** the **data flow** to **determine** whether GIF, JPEG, HTML or other types of files are being transmitted. Then, it optimizes the transmission...

... by bundling HTML content and graphics into a single package that is sent to the client. Our **tests** showed this compression caused no degradation in the resolution of the Web pages. The client-side software...

... an intelligent cache, which differs from a browser's static cache in that it can determine the **elements** of a Web page that remain constant and

request only the changed components. This feature is especially...
...link, the number of transmissions is reduced, which lowers latency. Using compression and caching, BoostWeb can increase **performance** by 50%. But by addressing latency as well, **performance** can improve two or threefold with any application you run across the Web. Implementing improvements BoostWeb Enterprise's...

... client loaded on a Windows 98 desktop and caching disabled on the browser, we experienced only moderate **performance** improvements, ranging from none to 20% (barely noticeable). When we moved the BoostWeb Enterprise client from the desktop machine to a proxy server, **performance** improved a little more over our WAN connection. On average, **performance** was double that without BoostWeb Enterprise, with surges occasionally boosting **performance** gains threefold. On the dial-up link, **performance** gains were similar in proportion but much more noticeable. Cutting the wait time for page downloads from...

... as was the case with the dial-up link - is more obvious than reducing a 1-second **delay** to a half-second. The installation and configuration was a little rougher than we expected. Our problems...

... core technology works, there are too many loose ends. Still, if you're not happy with the **performance** you're getting from your Web-based applications, BoostWeb Enterprise may work for you as an alternative...

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069284

Where do you think you're going?

Byline: Mark Gibbs, Network World Test Alliance

Journal: Network World Page Number: 57

Publication Date: October 05, 1998

Word Count: 1421 Line Count: 135

Text:

... Monitor what your users do. And if you want to enforce the policies, you'll need to **block** unauthorized Internet and intranet access. We looked at seven products designed to monitor and **block** user access to Web sites, Internet file archives, newsgroups and other Internet resources. Beyond monitoring and **blocking**, the products also generate usage reports that can help you with capacity planning. In our tests, a...

... placed a close second. Both products feature comprehensive monitoring and reporting capabilities. SessionWall-3's in-depth **blocking** ability gave it a slight edge over LittleBrother Pro. SessionWall-3 does more than just monitor your...

... very small networks because of their inherent architectures. Architecture strategies The seven products we tested tackle monitoring and **blocking** three different ways. Those that use a **packet** analyzer configuration run on a computer attached to a network. But rather than having network requests pass through them, **packet** analyzers capture and analyze **packets** as they pass by on the network. Leading scorers SessionWall-3 and LittleBrother Pro operate this way. In a **packet** analyzer configuration, if a **packet** contains an unauthorized request such as a banned Web site, the **program** immediately replies to the **packet**'s originator with a protocol exchange that terminates the transaction, effectively **blocking** the request. There are a couple downsides to this method. First, the **packet** analyzers can only "see" the network segment to which they are attached, requiring that a **packet** analyzer be placed on all segments of the network. In a switched network, **packet** analyzers must be placed on what is called the **diagnostic** or promiscuous mode port so all traffic can be seen. If the switch doesn't have such a port, the **packet** analyzer must be placed on the gateway machine. Second, the **packet** analyzers' platform must be powerful enough to **process** the captured

network traffic at a high enough rate so transactions aren't missed. That said, even if one or two **packets** are missed, it's highly probable that a **packet** analyzer will detect and control the vast majority of transactions because most IP exchanges, such as browser...

... to the relay software to the proxy server. A potential problem with relay configuration is that the **process** of accepting requests from clients, examining the requests, logging them and then passing the permitted requests to the Internet takes time and computing power.

Processing overhead can significantly slow down user response. The third approach to monitoring requires modifying the client **operating** system or applications. SOS Pro and NNPro are based on such client-side modifications. It's the most basic architecture used for monitoring and **blocking**, but it's also the least satisfactory. Rollout costs are significant, and support overhead is high. And because the monitoring and **blocking** system works inside client software, there is the potential for a whole range of bugs and unpredictable...

... these architectures - if you delete the mapping, management no longer works. For corporate IT purposes, relay and **packet** analyzer configurations are the most practical. Nuts and bolts Beyond the way these products integrate with the network is the issue of how they actually monitor and **block** access. Once the software has fielded a request for a URL, for example, the URL must be compared to a list of sites. The request can be tracked or tracked and **blocked**. Given the increasing number of Web sites on the Internet, determining which sites should be **blocked** is not something you want to take on alone. SurfWatch supplies a database of URLs that are... subscription. You update SessionWall-3 and SurfWatch databases from their management interfaces but, unfortunately, neither automates the **process**. WebSense and LittleBrother Pro incorporate a database updating system as part of their management interfaces, and both...

... twice monthly. Neither WizGuard Proxy Server nor SOS Pro offers a predefined list of URLs to be **blocked**. All the products with predefined databases allow you to customize their lists, but we found that locating...

... challenge. One way to identify unacceptable URLs is through independent ratings. SessionWall-3 and SOS Pro support **blocking** based on RSACi ratings created by the Recreational Software Advisory Council. However, because the RSACi rating scheme...

... undesirable sites, and they are methods that SessionWall-3, SurfWatch, SOS Pro and NNPro support. LittleBrother Pro **performs** only URL scanning, and WizGuard Proxy Server scans for pornography. However, while keyword scanning can help detect...

... words, it can't distinguish between inappropriate and legitimate sites, leading to overblocking. The technique may also **block** a page of results from search engines, even though the page contains links to permissible sites. You should make sure that all search engines you use are exempt from **blocking**. Managing the managers How you configure and manage the product is a vital ease-of-use issue. WizGuard...

... engineer, installation proceeded more smoothly. Although you may balk at yet another network management tool, monitoring and **blocking** are services you really should consider. Even if you never look at the logs, telling users that...

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NEWS BRIEFS

MOBILE SATELLITE NEWS

April 2, 1998 VOL: 10 ISSUE: 7 DOCUMENT TYPE: NEWSLETTER

PUBLISHER: PHILLIPS BUSINESS INFORMATION

LANGUAGE: ENGLISH

WORD COUNT: 1100

RECORD TYPE: FULLTEXT

TEXT:

...use the 2

GHz band are: Celsat Inc., the Boeing Co. [BA], Constellation Communications Inc., Mobile Communications **Holdings** Inc., Globalstar L.P. [GSTRF], Iridium LLC [IRIDF], ICO Services Ltd., TMI Communications and Co. L.P. and Inmarsat Horizons. The **process** will involve officials from the National Telecommunications and Information Administration (NTIA) because of that agency's primary...

...L.P. [GSTRF] has appointed Gloria Everett senior vice president operations, responsible for managing schedule, costs, and **performance** associated with the operation, integration and test, and deployment of Globalstar's worldwide, satellite-based communications system addition to voice and fax from remote **operating** sites. The unit can quickly be set up by a single person and operates from a wide range of input voltages and over a wide **operating** temperature range, making it suited to any environment. (Wade Sober, CSSI, 716/425-0760.)

Orbcomm Opens European...

...Services Inc. [JBHT], the largest public trucking company in the United States, has chosen AMSC's multi-**mode** messaging service to **track** its local truck fleets. The second deal involves Dart Transit Co., one of the nation's 10...

...customers will be able to use the AIS network. Fredrik Verkroost, ICO's chief commercial officer and **executive** vice ...the newly created position of vice president of its Evolved Expendable Launch Vehicle (EELV) and Delta IV **programs**. Kennedy was previously vice president for Delta III. "[Mike] brought the [Delta III] vehicle through development, testing and initial **program** on time-meeting our customers' requirements. He is now in a position to bring his skills to our EELV and Delta IV **programs**, ensuring their success," said Gale Schluter, vice president and general manager of Boeing's Expendable Launch Systems...